



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
Academic Year : 2018- 2019, Semester - I

CLASS TIME TABLE

Class : TE

Class Room : A-10

w.e.f : 20th June 2018

Time	MON	TUE	WED	THU	FRI	SAT
08:30 AM – 09:30 AM	MT (A-10)	MC (A-10)	DC (A-10)	ESD (A-10)	EM (A-10)	AC3
09:30 AM – 10:30 AM	EM (A-10)	T&P (A-10)	T&P (A-10)	MC (A-10)	MC (A-10)	
10:30 AM – 10:45 AM	SHORT RECESS					
10:45 AM – 11:45 AM	A3E - EM B3E - EM	A3E - MC B3E - DSP	A3E - DC B3E - ESD	A3E - MT B3E - DC	A3E - DSP B3E - MT	AC3
11:45 AM – 12:45 PM	C3E - DSP D3E - MT	C3E - MT D3E - DC	C3E - MC D3E - DSP	C3E - ESD D3E - MC	C3E - DC D3E - ESD	
12:45 PM – 01:30 PM	LONG RECESS					
01:30 PM – 02:30 PM	DC (A-10)	EM (A-10)	DSP (A-10)	MT (A-10)	A3E - ESD B3E - MC	EXTRA ACITIVITY
02:30 PM – 03:30 PM	ESD (A-10)	DSP (A-10)	MT (A-10)	DC (A-10)	C3E - EM D3E - EM	
03:30 PM – 04:30 PM	DSP (A-10)	DSP* (A-10)	MC* (A-10)	MT* (A-10)	DC* (A-10)	
04:30 PM – 05:30 PM	MENTOR MEETING	EM* (A-10)				

Note: * - Lecture allotted for slow learner

COURSE	NAME OF FACULTY	
	THEORY	PRACTICAL / TUTORIAL
DC: Digital Communication	Prof. Ashok Shinde	-
DSP: Digital Signal Processing	Prof. Ankita Agarwal	-
MC: Microcontrollers	Prof. Ashvini Kulkarni	-
EM: Electromagnetics	Prof. Sujata Virulkar	Prof. Sujata Virulkar
MT: Mechatronics	Prof. Varsha Degaonkar	-
ESD: Electronics System Design	Prof. Risil Chhatrala	Prof. Risil Chhatrala
DC & SP Lab	DC	Prof. Ashok Shinde
	DSP	Prof. Ankita Agarwal
MC & MT Lab	MC	Prof. Ashvini Kulkarni
	MT	Prof. Varsha Degaonkar
AC3: Audit Course 3		

Batch	Venue					
	DC	DSP	MT	MC	EM	ESD
A3E	A-115	A-112	A-04	A-202	A-10	A-102
B3E	A-115	A-112	A-04	A-202	A-10	A-102
C3E	A-115	A-112	A-04	A-202	A-10	A-102
D3E	A-115	A-112	A-04	A-202	A-10	A-102

Batch	Roll Nos.
	From - To
A3E	TE001 – TE020
B3E	TE021 – TE040
C3E	TE041 – TE060
D3E	TE061 Onwards

A Jagtap

Prof. Anjali A Jagtap
Time Table Co-Ordinator

S M Mahalakshmi Naidu

Dr. S M Mahalakshmi Naidu
Head of Department

Vat

Dr. Vaishali Patil
Principal

