



**International Institute of Information Technology,
Hinjawadi, Pune.
Department of Computer Engineering**



Report of "Faculty Development Program"

On "Digital Electronics & Logic Design"

Approved by BOS-Computer Engineering, SPPU, Pune

Department of Computer Engineering, International Institute of Information Technology had organized two days Faculty Development Program (FDP) on for the subject "Digital Electronics and Logic Design (T.E. Computer Engineering 2015 Course) on 17th & 18th June, 2016 in Association with BOS Computer Engineering, Savitribai Phule Pune University, Pune. This FDP received overwhelming response from participants from various Institutions.

Dr. G.V. Garje, Chairman BoS, Information Technology, SPPU was the Guest of Honor and **Prof. D.B. Kshirsagar, Subject Coordinator and Head of Department Computer Engineering, Sanjivani College of Engineering, Kopargaon** was the Chief Guest for the inauguration function. Dr. V.N. Patil (Principal I²IT), Prof. Ravindra Joshi (Dean Academics I²IT), Prof. Sandeep Patil (HoD Computer Engineering Department), Prof. Vilas Mankar (HoD Information Technology) and 90 participants from various engineering colleges affiliated to SPPU were present for the inauguration ceremony. Dr. G.V. Garje inaugurated the FDP and shared his views regarding teaching learning, evaluation and assessment process. Prof. D.B. Kshirsagar shared the message from Dr. Varsha Patil, Chairman BoS Computer Engineering regarding the syllabus revision process and importance of subject. Prof. D.B. Kshirsagar also shared his views about the conduction of the subject and asked all the faculties to contribute their knowledge and suggestions for the improvement in the DELD syllabus.

The day one of the FDP started with the Session conducted by Prof. Parag Hirulkar, Associate Professor Department of Electronics & Telecommunication, I²IT. He was accompanied by Prof. Meghana Joshi and Prof. Dipak Raut. This session gave participants information about the installation of Xilinx Software installation, introduction to HDL and basics of VHDL programming. During the session the participants were also given the hands on of VHDL programming. All the VHDL assignments from Group C were discussed during the session.

In the second session **Mr. Shripad Deshpande, VP Product Development, Syncspace Solutions Pvt. Ltd, Pune** discussed the assignments from Group B. Mr. Deshpande thoroughly covered the theory related to assignments as well the implementation.

The first session of day two of the FDP was conducted by **Prof. M.V. Nimbalkar, Associate Prof.- Information Technology Department Sinhgad College of Engineering, Vadgaon, Pune**. With his thorough knowledge of subject Prof. Nimbalkar guided the present faculties about the effective conduction of the subject. He initiated a very thoughtful discussion about the importance of

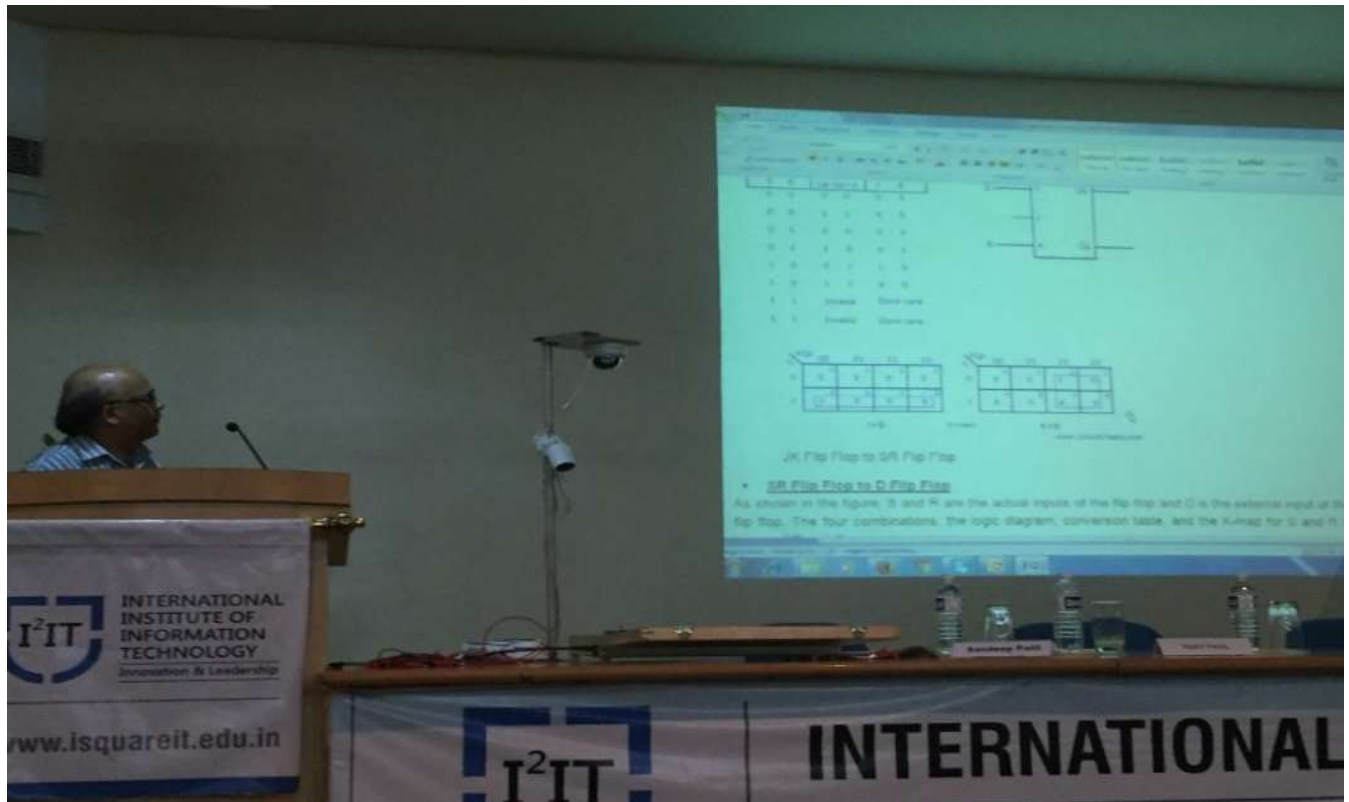
question bank and suggestions for the modification in the syllabus. Prof. Nimbalkar also discussed Group – A assignments and related theory. It was a very interactive session where the participants actively participated in the discussion.

The last session of the FDP was conducted by Prof. Bhushan Bhokse, Assistant Professor Information Technology Department, MIT Academy of Engineering, Alandi. Prof. Bhokse discussed the assignments from Group B. He also shared valuable information about the effective use of DigiWorks to create demonstrations of the DELD practical.

The FDP was concluded with a Valedictory function.

According to the feedback given by participants, they found this FDP very useful for teaching the subject.





Participants at the FDP



International Institute of Information Technology, Hinjawadi, Pune.



Department of Computer Engineering & Information Technology

Report of “Short Term Training Program” On “Advances in Machine Learning and Big Data” Sponsored by BCUD – SPPU under QIP Scheme

Department of Computer Engineering, International Institute of Information Technology had organized two days Short term training program (STTP) on for the topic “Advances in Machine Learning and Big Data” on 22nd&23rd December, 2016 sponsored by BCUD, Savitribai Phule Pune University, Pune. This STTP received overwhelming response from participants from various Institutions.

Dr. Pramod Patil, Member BoS Computer Engineering SPPU and Principal DYPCOE, Pimpri was the Chief Guest and **Dr. Aditya Abhyankar, Dean, Faculty of Technology, SPPU** was the Guest of Honor & Keynote speaker for the inauguration function. Dr. Vaishali Patil (Principal I²IT), Prof. Ravindra Joshi (Dean Academics I²IT), Dr. Sashikala Mishra (HoD Computer Engineering Department), and 35 participants from various engineering colleges from different parts of India were present for the inauguration ceremony. Dr. Pramod Patil inaugurated the STTP and shared his views about the importance of conducting the training program on machine learning and big data.

The day one of the STTP started with the entrancing keynote speech by Dr. Aditya Abhyankar. Dr. Abhyankar explained the importance of mathematics and unveiled the mathematical formulas in a very simple way. Dr. Abhyankar extended the topic to kernels and importance of kernels in machine learning.

In the second session **Mr. Vishnu Chevli, Data Scientist, KPIT** discussed the real time application using big data. Mr. Vishnu explained case studies of big data generated by mobiles for traffic management.

The last session of day one was conducted by Dr. Kailash Shaw. With his thorough knowledge of subject Dr. Shaw guided the present participants about the effective use of Matlab tools for

machine learning implementations. Dr. Shaw demonstrated several prominent algorithms in machine learning using simple Matlab tools and techniques. It was a very interactive session where the participants actively participated in the discussion.

The first session of day two of the STTP was conducted by Dr. Parag Deshpande, Associate Professor, VNIT Nagpur. Dr. Deshpande explained the Graph based algorithms in machine learning effectively with simple mathematical derivations. He also shared valuable information about the effective use of R programming to create models for graph based algorithms.

The last session of STTP was presented by Dr. Vahida Attar, Associate Professor, CoEP Pune. Madam discussed the topic "Big data and IoT". With several relevant statistics Dr. Attar explained the importance of big data in today's applications scenario. Dr. Attar effectively linked the relation between big data and the major application area of big data as IoT. She also discussed several applications of IoT in smart city development and role of big data in such applications.

The FDP was concluded with a Valedictory function.

According to the feedback given by participants, they found this FDP very useful for teaching the subject.







International Institute of Information Technology, Hinjawadi, Pune-57

Department of Electronics and Telecommunication

A Report On Faculty Orientation Workshop on “SE (E&TC/ Elex) Revised Syllabus 2015 Course”

A Faculty Orientation Workshop on **SE (E&TC/Elex) Revised Syllabus 2015 Course** for the **subject: Data Structures and Algorithms** was jointly organized by BoS Electronics, Savitribai Phule Pune University, Pune and International Institute of Information Technology between 9th June to 11th June 2016.

Objective:

The objective of this workshop was to orient all the faculty towards the revised syllabus of SE(E&TC/Elex) and to bring in uniformity in teaching across all the colleges under the SPPU.

DAY-I: Thursday, 09/06/2016. Session 01

Prof. Dr. G. R. Patil from AIT, Dighi, Pune (Contact Details: 9422510862; patilgr67@yahoo.co.in) have conducted a session on 1st unit (Introduction to C and Algorithm).

Topic Covered in the session are constants, variables, keywords, operators, control structure, functions, macros, arrays and string manipulation.

In Functions: Parameter passing call by value and call by reference, scope rules, functions and pointers, function returning pointer, pointer to function, String manipulations using Arrays, pointer to pointer, Dynamic memory management.

He has covered all the basics of C language very effectively and it was very engaging session.

DAY-I: Thursday, 09/06/2016. Session 02

Prof. S. R. Dhore from AIT, Dighi, Pune (Contact Details: 9890809251; hodcomp@aitpune.edu.in) have conducted a session on 2nd unit.

Topic Covered in the session are **Analysis of algorithm**: frequency count and its importance in analysis of an algorithm, Time complexity & Space complexity of an algorithm, Big ‘O’ notation in relation to searching and sorting.

The necessity of analysis of algorithms and its application for real life situations have motivated all faculty members to incorporate space and time complexity in every algorithm.

DAY-2: Friday, 10/06/2016. Session 01

Prof. V. V. Sovoni from PVG COE, Pune (Contact Details: 9422001630; vaidehisovani@gmail.com) have conducted a session on 3rd unit.

Topic Covered in the session are **Stack and Queues**. The implementation details of stacks and queues using array are covered well using standardized method of coding.

DAY-2: Friday, 10/06/2016. Session 02

Prof. Gouri Ghule from VIIT, Pune (Contact Details: 9850073434; gouri.ghule@viit.ac.in) have conducted a session on 4th unit.

Topic Covered in the session are Concept of linked organization, singly linked list, stack using linked list, queue using linked list, doubly linked list, circular linked list, Linked list as ADT. Representation and manipulations of polynomials using linked lists, comparison of sequential linked organization with linked organization.

DAY-3: Saturday, 11/06/2016. Session 01

Prof. S. B. Dhekale from AISSMS COE, Pune (Contact Details: 9049996282; sbdhekale@aiissmscoe.com) have conducted a session on 5th unit.

Topic Covered in the session are Introduction to trees: Basic Tree Concepts, Binary Trees: Concept & Terminologies, Representation of Binary Tree in memory, Traversing a binary tree, Binary Search Trees (BST): Basic Concepts, BST operations.

He has covered all the contents of fifth unit and has given emphasis on practical implementation by teaching faculty before going to class.

Prof. Nilesh Bhojne from SCOE has shared his valuable experience and motivated faculties towards innovative teaching methodology for the subject.

DAY-3: Saturday, 11/06/2016. Session 02

Prof. V. R. Lele from K. K. Wagh COE, Nashik (Contact Details: 9689907297; vrlele@kkwagh.edu.in) have conducted a session on 6th unit (Graphs).

Topic Covered in the session are Basic Concepts & terminology, Sequential representation of graphs; Adjacency matrix, Path matrix, Linked representation of a graph, Operations on graph, Traversing a graph, Spanning trees; Minimum Spanning tree, Kruskal's Algorithm, Prim's Algorithm. Dijkstra's Shortest Path Algorithm

Contact Person: Dr. G. R. Patil, Professor, AIT, Dighi, Pune

Mobile: 9422510862. **E-Mail:** patilgr67@yahoo.co.in

Google Group: DSA_2015course@googlegroups.com

Contact person: Mrs. Degaonkar Varsha, **Mobile:** 9371276775,

E-Mail: varshad@isquareit.edu.in