

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
Academic Year 2020 - 21, Semester - II

COURSE FILE INDEX

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30	CO Attainment through Class Tests	I2IT / ACAD / AT / 03
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Innovator & Learner

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
Academic Year: 2020-21, Semester - II

I2IT / ACAD / CP / 03 VER 01

THEORY TEACHING RECORD

Course Code :210252			Class:SE			Name of Faculty: Dr. Shitole Ajitkumar S.				
Course Name: Data Structures and Algorithms							Teaching Scheme:		Th : 3 Hrs / week	
Lr. No.	Topics to be Delivered		CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC	HOD	APMC
UNIT I : Hashing										
1	1.1	Subject Orientation	CEO213.3 CEO213.5	CO213.1	4/1/2021	4/1/2021				
2	1.2	Subject Orientation Unit No I:Hashing,Hash T			7/1/2021	7/1/2021				
3	1.3	Bucket, collision, probe, synonym			8/1/2021	8/1/2021				
4	1.4	overflow, open hashing, closed hashing.			18/1/2021	18/1/2021				
5	1.5	Perfect hash function, load density			19/1/2021	19/1/2021				
6	1.6	Issues in hashing, hash functions- properties of	CEO213.3 CEO213.5	CO213.1	20/1/2021	20/1/2021				
7	1.7	Collision resolution strategies- open addressing			21/1/2021	21/1/2021				
8	1.8	Hash table overflow- open addressing and chain			22/1/2021	22/1/2021				
9	1.9	Dictionary, Skip List- representation, searching			25/1/2021	25/1/2021				
10	1.10	Hashing Revision			27/1/2021	27/1/2021				
Monitored by										
								AC	HOD	APMC

UNIT II : Trees

11	1.1	Unit No II: Trees- Basic terminology, General	CEO213.3 CEO213.5 CEO213.6 CO213.2	28/1/2021	28/1/2021	
12	1.2	Binary tree traversals-in order, preorder, post order.		1/2/2021	1/2/2021	
13	1.3	Non recursive traversals.		1/2/2021	1/2/2021	
14	1.4	Level wise -depth first and breadth first		3/2/2021	3/2/2021	
15	1.5	Operations on binary tree. Binary Search Tree (BST), BST operations.		8/2/2021	8/2/2021	
16	1.6	BST deletion & dictionary implementation.		10/2/2021	10/2/2021	
17	1.7	Threaded binary tree- concepts, threading,		12/2/2021	12/2/2021	
18	1.8	In order traversal of in-order threaded binary		15/2/2021	15/2/2021	
19	1.9	Case Study- Use of binary tree in expression tree-evaluation and Huffman's coding		17/2/2021	17/2/2021	
20	1.10	Content Beyond Syllabus: Decision Tree		24/2/2021	24/2/2021	

UNIT III : Graphs

21	2.1	Unit No III: Graphs, Basic Concepts, Storage	CEO213.1 CEO213.2 CEO213.3 CEO213.6	22/2/2021	22/2/2021	
22	2.2	Adjacency list, adjacency multi list, inverse adjacency list.		24/2/2021	24/2/2021	
23	2.3	Traversals-depth first and breadth first, Introduction to Greedy Strategy.		1/3/2021	1/3/2021	
24	2.4	Minimum spanning Tree, Greedy algorithms		3/3/2021	3/3/2021	
25	2.5	Kruskal's Algorithms and Dijkstra's Single		5/3/2021	5/3/2021	
26	2.6	Topological ordering, Case Study- Data		8/3/2021	8/3/2021	
27	2.7	Content Beyond Syllabus: Bellman Ford Algorithm		10/3/2021	10/3/2021	

UNIT IV : Search Trees

28	4.1	Unit No IV: Search Trees, Symbol Table-	213.3CEO213.4	10/3/2021	10/3/2021	
29	4.2	Static tree table and Dynamic tree		17/3/2021	17/3/2021	
30	4.3	Introduction to Dynamic Programming.		9/4/2021	9/4/2021	
31	4.4	Height Balanced Tree- AVL tree.		12/4/2021	12/4/2021	
32	4.5	Height Balanced Tree- AVL tree and Splay		16/4/2021	16/4/2021	
33	4.6	Red Black Trees and AA trees		19/4/2021	19/4/2021	
34	4.7	K-D Trees		23/4/2021	23/4/2021	

UNIT V : Indexing and Multi way Trees

35	5.1	Unit No V: Indexing and Multi way Trees, Indexing and Multi way Trees- Indexing, indexing techniques.		19/5/2021	19/5/2021	
36	5.2	Types of search tree- Multi way search tree, B+	CO213.3 CO213.4	20/5/2021	20/5/2021	
37	5.3	B+ Tree Content Beyond Syllabus: Huffman Trees		21/5/2021	21/5/2021	
38	5.6	Heap-Basic concepts, realization of heap and operations, Heap as a priority queue.		24/5/2021	24/5/2021	
39	5.7	Downheaping Operation of Heap		24/5/2021	24/5/2021	
40	5.8	Content Beyond Syllabus: Heap Sort		25/5/2021	25/5/2021	

UNIT VI : File Organization

40	6.1	Unit No VI: File Organization, Sequential file organization- concept and primitive operations.		25/5/2021	25/5/2021	
41	6.2	Direct Access File- Concepts and Primitive operations.	CO213.3 CO213.4	26/5/2021	26/5/2021	
42	6.3	Indexed sequential file organization-concept.		26/5/2021	26/5/2021	
43	6.4	Types of indices, structure of index sequential file, Linked Organization- multi list files		26/5/2021	26/5/2021	
44	6.5	Coral rings, inverted files and cellular partitions.		27/5/2021	27/5/2021	
45	6.6	Multi way merging- a k way merge algorithm.		28/5/2021	28/5/2021	

Start of Semester

Signature	Date
	4/5/21
Course Faculty :	
HoD :	6.1.2021

End of Semester

Signature	Date
	16/6/21
Course Faculty :	
HoD :	16.6.21



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
Academic Year : 2020-21, Semester - II

PRACTICAL TEACHING RECORD

Subject Code : 210256		Class:SE		Name of Faculty: Shitole Ajitkumar S.		Pr : 2 Hrs / week	
Subject Name: Data Structures & Algorithms Lab.		Batch: A		Teaching Scheme:		Monitored by	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC HOD APMC
1	Consider telephone book database of N clients. Make use of a hash table implementation to quickly look up client's telephone number. Make use of two collision handling techniques and compare them using number of comparisons required to find a set of telephone numbers. (Use Python Programming Language)	CEO217.3	CO217.1	18/4/2021	18/1/2021		
2	Write a program in C++ to implement dictionary using hashing. Use separate chaining and perform insertion, display, search and deletion operations.	CEO217.3	CO217.1	25/01/2021 01/02/2021	25/01/2021 01/02/2021		
3	Convert given binary tree into threaded binary tree. Analyze time and space complexity of the algorithm.	CEO217.1 CEO217.3	CO217.2	15/02/2021 17/02/2021	15/02/2021 17/02/2021		
4	Represent a given graph using adjacency matrix/list to perform DFS and using adjacency list to perform BFS. Use the map of the area around the college as the graph. Identify the prominent land marks as nodes and	CEO217.1 CEO217.3	CO217.3	22/02/2021 01/03/2021	22/02/2021 01/03/2021		

5	Represent a given graph using adjacency matrix to find length of shortest path between every pair of vertices. Use Floyd Warshall's algorithm to implement it.	CEO217.1 CEO217.3	CO217.3	8/3/2021	8/3/2021		
6	A Dictionary stores keywords and its meanings. Provide facility for adding new keywords, deleting keywords, updating values of any entry. Provide facility to display whole data sorted in ascending/Descending order. Also find how many maximum comparisons may require for finding any keyword. Use Height balance tree and find the complexity for finding a keyword	CEO217.2	CO217.5	12/4/2021 19/4/2021	12/4/2021 19/4/2021		
7	Department maintains a student information. The file contains roll number, name, division and address. Allow user to add, delete information of student. Display information of particular employee. If record of student does not exist an appropriate message is displayed. If it is, then the system displays	CEO217.3	CO217.6	26/4/2021	26/4/2021		
8	Design a group mini project	CEO217.3	CO217.4	28/5/2021	28/5/2021		

Start of Semester

Signature	Date
	4/1/21
Course Faculty :	
HoD :	6.1.21

End of Semester

Signature	Date
	16/6/21
Course Faculty :	
HoD :	16.6.21



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
Academic Year : 2020-21, Semester - II

Foundation & Learning

PRACTICAL TEACHING RECORD

Subject Code : 210256			Class:SE		Name of Faculty: Shitole Ajitkumar S.			Pr : 2 Hrs / week					
Subject Name: Data Structures & Algorithms Lab.						Teaching Scheme:			Monitored by				
Sr. No.			Experiment / Assignment		CEO	CO	Batch: B	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC	HOD	APMC
1			Consider telephone book database of N clients. Make use of a hash table implementation to quickly look up client's telephone number. Make use of two collision handling techniques and compare them using number of comparisons required to find a set of telephone numbers. (Use Python Programming Language)		CEO217.3	CO217.1	20/1/2021	20/1/2021					
2			Write a program in C++ to implement dictionary using hashing. Use separate chaining and perform insertion, display, search and deletion operations.		CEO217.3	CO217.1	25/01/2021 27/01/2021 03/02/2021	25/01/2021 27/01/2021 03/02/2021					
3			Convert given binary tree into threaded binary tree. Analyze time and space complexity of the algorithm.		CEO217.1 CEO217.3	CO217.2	10/02/2021 17/02/2021	10/02/2021 17/02/2021					
4			Represent a given graph using adjacency matrix/list to perform DFS and using adjacency list to perform BFS. Use the map of the area around the college as the graph. Identify the prominent land marks as nodes and		CEO217.1 CEO217.3	CO217.3	24/02/2021 03/03/2021	24/02/2021 03/03/2021					
5			Represent a given graph using adjacency matrix to find length of shortest path between every pair of vertices. Use Floyd Warshall's algorithm to implement it.		CEO217.1 CEO217.3	CO217.3	10/03/2021 17/03/2021	10/03/2021 17/03/2021					

18/6/21

16.6.21

6	A Dictionary stores keywords and its meanings. Provide facility for adding new keywords, deleting keywords, updating values of any entry. Provide facility to display whole data sorted in ascending/ Descending order. Also find how many maximum comparisons may require for finding any keyword. Use Height balance tree and find the complexity for finding a keyword	CEO217.2	CO217.5	1/202124/5/2021	202124/5/20				
7	Department maintains a student information. The file contains roll number, name, division and address. Allow user to add, delete information of student. Display information of particular employee. If record of student does not exist an appropriate message is displayed. If it is, then the system displays the student details. Use sequential file to	CEO217.3	CO217.6	26/4/2021	26/4/2021				
8	Design a group mini project	CEO217.3	CO217.4	28/5/2021	28/5/2021				

Start of Semester

Signature	Date
Course Faculty :	4/5/24
HoD :	6.1.24

End of Semester

Signature	Date
Course Faculty :	16/6/24
HoD :	16.6.24



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
 Academic Year : 2020-21, Semester - II

PRACTICAL TEACHING RECORD

Subject Code : 210256			Class:SE		Name of Faculty: Shitole Ajitkumar S.					
Subject Name: Data Structures & Algorithms Lab.					Batch: C		Teaching Scheme:		Pr : 2 Hrs / week	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC	HOD	Monitored by	
1	Consider telephone book database of N clients. Make use of a hash table implementation to quickly look up client's telephone number. Make use of two collision handling techniques and compare them using number of comparisons required to find a set of telephone numbers. (Use Python Programming Language)	CEO217.3	CO217.1	22/1/2021	22/1/2021					
2	Write a program in C++ to implement dictionary using hashing. Use separate chaining and perform insertion, display, search and deletion operations.	CEO217.3	CO217.1	5/2/2021	5/2/2021					
3	Convert given binary tree into threaded binary tree. Analyze time and space complexity of the algorithm.	CEO217.1 CEO217.3	CO217.2	12/02/2021 26/02/2021	12/02/2021 26/02/2021					
4	Represent a given graph using adjacency matrix/list to perform DFS and using adjacency list to perform BFS. Use the map of the area around the college as the graph. Identify the prominent land marks as nodes and	CEO217.1 CEO217.3	CO217.3	05/03/2021 12/03/2021	05/03/2021 12/03/2021					
5	Represent a given graph using adjacency matrix to find length of shortest path between every pair of vertices. Use Floyd Warshall's algorithm to implement it.	CEO217.1 CEO217.3	CO217.3	19/3/2021 9/4/2021	19/3/2021 9/4/2021					

6	A Dictionary stores keywords and its meanings. Provide facility for adding new keywords, deleting keywords, updating values of any entry. Provide facility to display whole data sorted in ascending/Descending order. Also find how many maximum comparisons may require for finding any keyword. Use Height balance tree and find the complexity for finding a keyword	CEO217.2	CO217.5	16/4/2021 23/4/2021	16/4/2021 23/4/2021				
7	Department maintains a student information. The file contains roll number, name, division and address. Allow user to add, delete information of student. Display information of particular employee. If record of student does not exist an appropriate message is displayed. If it is, then the system displays the student details. Use sequential file to maintain the data.	CEO217.3	CO217.6	26/4/2021	26/4/2021				
8	Design a group mini project	CEO217.3	CO217.4	28/5/2021	28/5/2021				

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End of Semester

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International Institute of Information Technology, Pune
DEPARTMENT OF COMPUTER ENGINEERING
 Academic Year : 2020-21, Semester - II

PRACTICAL TEACHING RECORD

Subject Code : 210256		Class:SE		Name of Faculty: Shitole Ajitkumar S.		Pr : 2 Hrs / week		
Subject Name: Data Structures & Algorithms Lab.		Batch: D		Teaching Scheme:		Monitored by		
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	AC	HOD APMC
1	Consider telephone book database of N clients. Make use of a hash table implementation to quickly look up client's telephone number. Make use of two collision handling techniques and compare them using number of comparisons required to find a set of telephone numbers. (Use Python Programming Language)	CEO217.3	CO217.1	4/2/2021	4/2/2021			
2	Write a program in C++ to implement dictionary using hashing. Use separate chaining and perform insertion, display, search and deletion operations.	CEO217.3	CO217.1	11/2/2021	11/2/2021			
3	Convert given binary tree into threaded binary tree. Analyze time and space complexity of the algorithm.	CEO217.1 CEO217.3	CO217.2	18/2/2021	18/02/2021			
4	Represent a given graph using adjacency matrix/list to perform DFS and using adjacency list to perform BFS. Use the map of the area around the college as the graph. Identify the prominent land marks as nodes and	CEO217.1 CEO217.3	CO217.3	25/2/2021 18/3/2021	2/202118/3/2021			

5	Represent a given graph using adjacency matrix to find length of shortest path between every pair of vertices. Use Floyd Warshall's algorithm to implement it.	CEO217.1 CEO217.3	CO217.3	1/4/2021	1/4/2021				
6	A Dictionary stores keywords and its meanings. Provide facility for adding new keywords, deleting keywords, updating values of any entry. Provide facility to display whole data sorted in ascending/Descending order. Also find how many maximum comparisons may require for finding any keyword. Use Height balance tree and find the	CEO217.2	CO217.5	15/4/2021 22/4/2021	15/4/2021 22/4/2021				
7	Department maintains a student information. The file contains roll number, name, division and address. Allow user to add, delete information of student. Display information of particular employee. If record of student does not exist an appropriate message is displayed. If it is, then the system displays the student details. Use sequential file to maintain the data.	CEO217.3	CO217.6	26/4/2021	26/4/2021				
8	Design a group mini project	CEO217.3	CO217.4	28/5/2021	28/5/2021				

Start of Semester

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End of Semester

Signature	Date
	16/6/21
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HoD :	16.6.21



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DEPARTMENT OF ENGINEERING SCIENCES
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S. No.	Format	DISCRIPTION
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15	Class Test Attendance	I2IT / ACAD / CP / 08
16	Class Test Evaluation Record	I2IT / ACAD / CP / 12
17	Content Beyond Syllabus	I2IT / ACAD / CP / 17
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Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ENGINEERING SCIENCES
Academic Year: 2020-21, Semester - II

12/IT / ACAD / CP / 03 VER 01

THEORY TEACHING RECORD

Course Code :107008			Class: FE (IT)		Name of Faculty: Prof. Rupali Yeole					
Course Name: Engineering Mathematics-II					Teaching Scheme:		Th : 4 Hrs / week			
Lr. No.	Topics to be Delivered		CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	Monitored by		
								AC	HOD APMC	
UNIT 1 : DIFFERENTIAL EQUATIONS										
1	1.1	Introduction to Differential Equations, order, degree of differential Equation.	CEO108.1	CO108.1	18/05/2021	18/05/2021				
2	1.2	Formation of Differential Equations			18/05/2021	18/05/2021				
3	1.3	Variable Separable form Differential			20/05/2021	20/05/2021				
4	1.4	Homogeneous D.E., Non homogeneous D.E. reducible to homogeneous D. E.			20/05/2021	20/05/2021				
5	1.5	Exact D.E.			21/05/2021	21/05/2021				
6	1.6	Reducible to Exact D.E.			21/05/2021	21/05/2021				
7	1.7	Reducible to Exact D.E.			21/05/2021	21/05/2021				
8	1.8	Linear D.E.			25/05/2021	25/05/2021				
9	1.9	Reducible to Linear D.E.			27/05/2021	27/05/2021				
UNIT 2 : APPLICATION OF DIFFERENTIAL EQUATION										
10	2.1	Applications of D.E. to find orthogonal trajectories	CEO108.2	CO108.2	28/05/2021	28/05/2021				
11	2.2	Newton's Law of Cooling			28/05/2021	28/05/2021				
12	2.3	Kirchoff's Law of Electrical circuits			28/05/2021	28/05/2021				
13	2.4	Kirchoff's Law of Electrical circuits			01/06/2021	01/06/2021				
14	2.5	Rectilinear Motion			01/06/2021	01/06/2021				
15	2.6	Rectilinear Motion			02/06/2021	02/06/2021				
16	2.7	Simple Harmonic Motion			02/06/2021	02/06/2021				
17	2.8	Fourier's Law of heat conduction			03/06/2021	03/06/2021				
18	2.9	Fourier's Law of heat conduction			03/06/2021	03/06/2021				

12IT / ACAD / C

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by	
							AC	HOD APMC
UNIT 3 : INTEGRAL CALCULUS								
19	3.1 Reduction formula for standard trigonometric functions	CEO108.3	CO108.3	04/06/2021	04/06/2021			
20	3.2 Reduction formula problems			04/06/2021	04/06/2021			
21	3.3 Gamma functions and problems			08/06/2021	08/06/2021			
22	3.4 Beta functions			08/06/2021	08/06/2021			
23	3.5 More problems on Beta functions			09/06/2021	09/06/2021			
24	3.6 Differentiation under Integral sign-I			10/06/2021	10/06/2021			
25	3.7 Differentiation under Integral sign-II			11/06/2021	15/06/2021			
26	3.8 Error functions.			15/06/2021	16/06/2021			
27	3.9 Error functions.			16/06/2021	16/06/2021			
UNIT 4 : CURVE TRACING								
28	4.1 Tracing of Cartesian Curves	CEO108.4	CO108.4	17/06/2021	17/06/2021			
29	4.2 Tracing of Cartesian Curves			18/06/2021	18/06/2021			
30	4.3 Tracing of polarCurves			22/06/2021	28/06/2021			
31	4.4 Tracing of polar Curves			29/06/2021	29/06/2021			
32	4.5 Tracing of Rose curves			30/06/2021	30/06/2021			
33	4.6 Tracing of Rose curves			01/07/2021	01/07/2021			
34	4.7 Tracing of Parametric curves			01/07/2021	01/07/2021			
35	4.8 Rectification of Cartesian curves			02/07/2021	02/07/2021			
36	4.9 Rectification of Cartesian curvesRectification of Polar and Parametric curves			06/07/2021	05/07/2021			
UNIT 5 : SOLID GEOMETRY								
37	5.1 Relation between Co-ordinate system	CEO108.5	CO108.5	07/07/2021	06/07/2021			
38	5.2 Sphere: Introduction and Different Forms			08/07/2021	07/07/2021			
39	5.3 General equation of Sphere,Touching the Spheres			09/07/2021	08/07/2021			
40	5.4 Orthogonal Sphere and Tangent Plane to Sphere			13/07/2021	09/07/2021			
41	5.5 Cone: Introductin and Different types of Cone			14/07/2021	12/07/2021			

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
42	5.6 Right Circular Cone			14/07/2021	13/07/2021				
43	5.7 Cylinder: Introduction and Different types of Cylinder			15/07/2021	14/07/2021				
44	5.8 Right Circular Cylinder			15/07/2021	15/07/2021				
45	5.9 Discussion on some miscellaneous examples			16/07/2021	15/07/2021				
UNIT 6 : MULTIPLE INTEGRALS & APPLICATIONS									
46	6.1 Introduction and Evaluation of Double Integration			20/07/2021	19/07/2021				
47	6.2 Evaluation of Double Integral when Limit is not given			20/07/2021	19/07/2021				
48	6.3 Change of Order of Integration			21/07/2021	20/07/2021				
49	6.4 Transformation to polar form			21/07/2021	20/07/2021				
50	6.5 Area of curves: Application	CEO108.6	CO108.6	22/07/2021	21/07/2021				
51	6.6 Evaluation of Triple Integration			23/07/2021	22/07/2021				
52	6.7 Evaluation of Triple Integral when the Limits are not given, Volume			23/07/2021	23/07/2021				
53	6.8 Mass, Mean value and RMS value			27/07/2021	23/07/2021				
54	6.9 Centre of Gravity and Moment of Inertia			27/07/2021	23/07/2021				

Start of Semester

Signature	Date
Course Faculty: <i>R. Raju</i>	
HoD: <i>R. Raju</i>	18/05/21

End of Semester

Signature	Date
Course Faculty: <i>P. P. P.</i>	
HoD: <i>P. P. P.</i>	24/07/21



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ENGINEERING SCIENCES
 Academic Year : 2020-21, Semester - II

PRACTICAL TEACHING RECORD

Subject Code :107008			Class:FE(IT)		Name of Faculty: Prof. Rupali Yeole						
Subject Name:Engineering Mathematics -II					Batch: All		Teaching Scheme:		Pr : 2 Hrs / week		
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	Monitored by				
							AC	HOD	APMC		
1	DIFFERENTIAL EQUATIONS	CEO108.1	CO108.1	1/6/2021	1/6/2021		gn				
2	APPLICATION OF DIFFERENTIAL EQUATION	CEO108.1 CEO108.2	CO108.2	08/06/2021 15/06/2021							
3	INTEGRAL CALCULUS	CEO108.3	CO108.3	29/06/2021							
4	CURVE TRACING	CEO108.4	CO108.4	06/07/2021 13/07/2021							
5	SOLID GEOMETRY	CEO108.5	CO108.5	20/07/2021							
6	MULTIPLE INTEGRALS & APPLICATIONS	CEO108.3 CEO108.4 CEO108.6	CO108.6	27/07/2021							

Start of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	
HoD : <i>[Signature]</i>	18/05/21

End of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	
HoD : <i>[Signature]</i>	24/07/21



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ENGINEERING SCIENCES
 Academic Year : 2020-21, Semester - II

PRACTICAL TEACHING RECORD

Subject Code :107008			Class:FE(IT)		Name of Faculty: Prof. Rupali Yeole				
Subject Name:Engineering Mathematics -II			Batch: B11		Teaching Scheme:		Pr : 2 Hrs / week		
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (increase of variance)	AC	HOD	APMC
1	DIFFERENTIAL EQUATIONS	CEO108.1	CO108.1	4/6/2021	4/6/2021				
2	APPLICATION OF DIFFERENTIAL EQUATION	CEO108.1 CEO108.2	CO108.2	11/06/2021 18/06/2021					
3	INTEGRAL CALCULUS	CEO108.3	CO108.3	02/07/2021					
4	CURVE TRACING	CEO108.4	CO108.4	09/07/2021 16/07/2021					
5	SOLID GEOMETRY	CEO108.5	CO108.5	23/07/2021					
6	MULTIPLE INTEGRALS & APPLICATIONS	CEO108.3 CEO108.4 CEO108.6	CO108.6	30/07/2021					

Start of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	
HoD : <i>[Signature]</i>	18/05/21

End of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	
HoD : <i>[Signature]</i>	24/07/21

COURSE FILE INDEX

S. No.	Format	DISCRIPTION
1	Faculty wise Time Table	I2IT / ACAD / TT / 02
2	Course Objectives and Outcomes (Theory)	I2IT / ACAD / CP / 01
3	Correlation of COs with POs (Theory)	I2IT / ACAD / CP / 02
4	Course Objectives and Outcomes (Laboratory)	I2IT / ACAD / CP / 01
5	Correlation of COs with POs ((Laboratory)	I2IT / ACAD / CP / 02
6	University Syllabus	
7	Previous University Question Papers	
8	Theory Question Bank	
9	Objective Question Bank	
10	Theory Teaching Plan	I2IT / ACAD / CP / 03
11	Laboratory Teaching Plan	I2IT / ACAD / CP / 04
12	Lesson Plan and Resources	I2IT / ACAD / CP / 05
13	Rubrics for Continuous evaluation	I2IT / ACAD / CP / 06
14	Class Test Question Papers with solutions	I2IT / ACAD / CP / 07
15	Class Test Attendance	I2IT / ACAD / CP / 08
16	Slow Learner and advanced Learner Identification	I2IT / ACAD / CP / 09
17	Schedule of slow Learner Activities	I2IT / ACAD / CP / 10
18	Assignments to Advanced Learners	I2IT / ACAD / CP / 11
19	Class Test Evaluation Record	I2IT / ACAD / CP / 12
20	List of Slow Learners	I2IT / ACAD / CP / 13
21	List of Advanced Learners	I2IT / ACAD / CP / 14
22	Slow Learner Attendance Record	I2IT / ACAD / CP / 15
23	Performance Improvement of Slow Learner	I2IT / ACAD / CP / 16
24	Content Beyond Syllabus	I2IT / ACAD / CP / 17
25	Theory Attendance Record	I2IT / ACAD / BB / 01
26	Practical Attendance Record	I2IT / ACAD / BB / 02
27	Continuous Assessment Record	I2IT / ACAD / BB / 03
28	Average University Result	I2IT / ACAD / AT / 01
29	CO Attainment through University Result	I2IT / ACAD / AT / 02
30	CO Attainment through Class Tests	I2IT / ACAD / AT / 03
31	CO Attainment through Course End Survey	I2IT / ACAD / AT / 04
32	CO Attainment through Continuous Evaluation	I2IT / ACAD / AT / 05
33	PO & PSO Attainment through CO for Theory	I2IT / ACAD / AT / 06
34	PO & PSO Attainment through CO for Practical	I2IT / ACAD / AT / 06
35	TW Calculation Sheet	I2IT / ACAD / AT / 07



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
Academic Year : 2020-21, Semester - I

I2IT / ACAD / CI

Class: SE	Course: Digital Circuits
Course Code: 204182	Faculty Name: Anjali Jagtap

COURSE EDUCATIONAL OBJECTIVES

Course Objective	Description
CEO203.1	The fundamental principles of two-valued logic and devices used to implement logical operations on variables.
CEO203.2	Boolean algebra, Karnaugh maps and its application to the design and characterization of digital circuits.
CEO203.3	To analyze logic processes and implement logical operations using combinational logic circuits.
CEO203.4	The principles of logic design and use of simple memory devices, flip-flops, and sequential circuits.
CEO203.5	Concepts of sequential circuits and to analyze sequential systems in terms of state machines.
CEO203.6	System design approach using programmable logic devices.

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO203.1	Understand classification, characteristics of digital logic families and design of CMOS and TTL logic gates.
CO203.2	Obtain/minimize expression using reduction techniques and understand design arithmetic logic circuits.
CO203.3	Design combinational logic circuits.
CO203.4	Understand design and working of flip-flops and Design sequential logic circuits.
CO203.5	Design finite state machine using Mealy and/or Moore machine.
CO203.6	Design digital system using Programmable logic design devices.

CLASS TEST- COURSE OUTCOME MAPPING

CLASS TEST - I			CLASS TEST-II			CLASS TEST - III		
Question No	CO Mapping	Marks	Question No	CO Mapping	Marks	Question No	CO Mapping	Marks
Q1 TO Q7	CO203.1	10	Q1 TO Q9	CO203.3	10	Q1 TO Q10	CO203.5	10
Q8 TO Q15	CO203.2	10	Q10 TO Q19	CO203.4	10	Q11 TO Q20	CO203.6	10

Signature of Faculty

Head of Department



Hope Foundation's
International Institute of Information Technology, Pune I2IT / ACAD / CP / 02 V6
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
ACADEMIC YEAR : 2020-21, SEM - I

CORRELATION BETWEEN CO, PO & PSO

Course Name: Digital Circuits
Faculty Name: Anjali A Jagtap

Course Code:204182
Class: SE

CO-PO MATRIX

Course Outcome (COs)	Program Outcomes (POs)											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO203.1	1	0	0	0	0	0	0	0	0	0	0	0
CO203.2	2	0	0	0	0	0	0	0	0	0	0	0
CO203.3	2	0	0	0	0	0	0	0	0	0	0	0
CO203.4	2	0	0	0	0	0	0	0	0	0	0	0
CO203.5	3	0	0	0	0	0	0	0	0	0	0	0
CO203.6	3	0	0	0	0	0	0	0	0	0	0	0

1 - LOW, 2 - MEDIUM, 3 - HIGH

Course Outcome (COs)	Program Specific Outcomes (PSOs)			
	PSO1	PSO2	PSO3	PSO4
CO203.1	1	0	0	0
CO203.2	3	0	0	0
CO203.3	3	0	0	0
CO203.4	3	0	0	0
CO203.5	3	0	0	0
CO203.6	3	0	0	0

1 - LOW, 2 - MEDIUM, 3 - HIGH


Signature of Faculty


Head of Department

Class: SE	Course: Digital Circuits Lab
Course Code: 204186	Faculty Name: Anjali A Jagtap

COURSE EDUCATIONAL OBJECTIVES

Course Objective	Description
CEO207.1	The fundamental principles of two-valued logic and various devices used to implement logical operations on variables.
CEO207.2	Boolean algebra, Karnaugh maps and its application to the design and characterization of digital circuits.
CEO207.3	To analyze logic processes and implement logical operations using combinational logic circuits.
CEO207.4	The principles of logic design and use of simple memory devices, flip-flops, and sequential circuits.

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO207.1	Design and implement multiplexer and demultiplexer circuits using digital IC.
CO207.2	Design and implement adder/subtractor and comparator circuits using digital IC.
CO207.3	Design and implement asynchronous and synchronous counters using digital IC.
CO207.4	Implement TTL and CMOS logic gate using digital IC and verify current voltage parameters referring datasheet.
CO207.5	Design and implement shift register and its application using digital IC.
CO207.6	Implement digital electronics experiments using virtual labs developed by MHRD

ASSIGNMENT- COURSE OUTCOME MAPPING

Assignment	Course Outcome No	Assignment	Course Outcome No
1	CO207.1	7	CO207.3
2		8	CO207.4
3		9	CO207.5
4	CO207.2	10	
5		11	CO207.1
6	CO207.3	12	CO207.3

Signature of Faculty

Head of Department



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION'
ACADEMIC YEAR : 2020-21, SEM - I
CORRELATION BETWEEN CO, PO & PSO

Course Name: Digital Circuits Lab	Course Code: 204186
Faculty Name: Anjali A Jagtap	Class: SE

CO-PO MATRIX

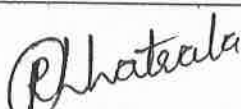
Course Outcome (COs)	Program Outcomes (POs)											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO207.1	1	0	1	0	2	0	0	0	3	2	0	1
CO207.2	2	0	1	0	2	0	0	0	3	2	0	1
CO207.3	2	0	1	0	2	0	0	0	3	2	0	1
CO207.4	2	0	1	0	2	0	0	0	3	2	0	1
CO207.5	2	0	1	0	2	0	0	0	3	2	0	1
CO207.6	2	0	1	0	2	0	0	0	3	2	0	1

1 - LOW, 2 - MEDIUM, 3 - HIGH

Course Outcome (COs)	Program Specific Outcomes (PSOs)			
	PSO1	PSO2	PSO3	PSO4
CO207.1	3	0	1	0
CO207.2	3	0	1	0
CO207.3	3	0	1	0
CO207.4	3	0	1	0
CO207.5	3	0	1	0
CO207.6	3	0	1	0

1 - LOW, 2 - MEDIUM, 3 - HIGH


Signature of Faculty


Head of Department



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
Academic Year: 2020-21, Semester - I

THEORY TEACHING RECORD

Course Code : 204182			Class:SE			Name of Faculty: Anjali A Jagtap			
Course Name: Digital Circuits			Teaching Scheme:				Th : 3 Hrs / week		
Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	AC	HOD	APMC
UNIT 1 : Digital Logic Families									
1	1.1 Families: Speed, power dissipation, figure of merit, fan in, fan out, current, voltage, noise immunity, operating temperatures and power supply requirements. TTL logic.			01.07.2020	01.07.2020				
2	1.2 Operation of TTL NAND gate, active pull up, wired AND,			02.07.2020	02.07.2020				
3	1.3 TTL NAND open collector output, unconnected inputs. Tri-State logic. CMOS logic: CMOS inverter,	CEO203.1	CO203.1	03.07.2020	03.07.2020				
4	1.4 CMOS NAND, NOR gates design			07.07.2020	07.07.2020				
5	1.5 CMOS unconnected inputs, wired logic, open drain output. Interfacing CMOS and TTL, Data sheet specifications.			08.07.2020	08.07.2020				
UNIT 2 : Combinational Logic Design									
6	2.1 Definition of combinational logic, canonical forms, Standard representations for logic functions,			09.07.2020	09.07.2020				
7	2.2 k-map representation of logic functions (SOP and POS forms),			10.07.2020	10.07.2020				
8	2.3 minimization of logical functions for min-terms (upto 4 variables).			14.07.2020	14.07.2020				

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
9	2.4 minimization of logical functions for max-terms (upto 4 variables),	CEO203.2	CO203.2	15.07.2020	15.07.2020				
10	2.5 don't care conditions,			16.07.2020	16.07.2020				
11	2.6 Examples practice			17.07.2020	17.07.2020				
12	2.7 Design Examples: Arithmetic Circuits, BCD to 7 segment decoder, Code converters.			20.07.2020	20.07.2020				
13	2.8 Introduction to Quine-McCluskey method,			22.07.2020	22.07.2020				
14	2.9 Quine McCluskey using don't care terms,			23.07.2020	23.07.2020				
	Class Test I			20.08.2020	20.08.2020				
UNIT 3 : Combinational Circuits									
15	3.1 Adders and their use as subtractor			27.07.2020	27.07.2020				
16	3.2 look ahead carry, ALU,			29.07.2020	29.07.2020				
17	3.3 Digital Comparator, Parity generators/checkers,			30.07.2020	30.07.2020				
18	3.4 Multiplexers and their use in combinational	CEO203.3	CO203.3	05.08.2020	05.08.2020				
19	3.5 logic designs, multiplexer trees, De-multiplexers and their use in combinational logic designs,			06.08.2020	06.08.2020				
20	3.6 Examples practice			12.08.2020	12.08.2020				
21	3.7 Decoders, Demultiplexer trees.			13.08.2020	13.08.2020				
UNIT 4 : Sequential Logic Design									
22	4.1 1 Bit Memory Cell, Clocked SR, JK, MS J-K. flip flop, D and T flip-flops. Use of preset and clear terminals, hold and setup time and metastability.			14.08.2020	14.08.2020				
23	4.2 Excitation Table for flip flop, Conversion of flip flops,			19.08.2020	18.08.2020				
24	4.3 Typical data sheet specifications of Flip flop application of Flip flops. Registers, Shift registers,	CEO203.4	CO203.4	20.08.2020	19.08.2020				
25	4.4 Counters (ring counters, twisted ring counters),			26.08.2020	20.08.2020				
26	4.5 ripple counters, Mod-n counters, up/down			27.08.2020	01.09.2020				

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
27	4.6 counters,			02.09.2020	03.09.2020		AC	HOD	APMC
28	4.7 synchronous counters, lock out, Clock Skew,			03.09.2020	03.09.2020				
29	4.8 Clock jitter. Effect on synchronous designs,			09.09.2020	03.09.2020				
30	4.9 Sequence Generators.			10.09.2020	08.09.2020				
31	4.10 Class Test II			16.09.2020	09.09.2020				
				17.09.2020	04.11.2020				
UNIT 5 : State Machines									
32	5.1 Basic design steps- State diagram, State table, State reduction, State assignment,			23.09.2020	10.09.2020				
33	5.2 Mealy and Moore machines representation,			24.09.2020	16.09.2020				
34	5.3 Examples practice			30.09.2020	17.09.2020				
35	5.4 Design of Sequence detector.			01.10.2020	23.09.2020				
36	5.5 Introduction to Algorithmic state machines-			07.10.2020	30.09.2020				
37	5.6 construction of ASM chart and realization for			08.10.2020	01.10.2020				
38	5.7			14.10.2020	07.10.2020				
UNIT 6 : Programmable Logic Devices									
39	6.1 Programmable logic devices: Detail architecture,			15.20.2020	08.10.2020				
40	6.2 Study of PROM, PAL, PLA,			21.10.2020	22.10.2020				
41	6.3 Designing combinational circuits using PLDs.			22.10.2020	22.10.2020				
42	6.4			26.10.2020	26.10.2020				
43	6.5 General Architecture, features and typical specifications of FPGA and CPLD.			28.10.2020	27.10.2020				
44	6.6 Semiconductor memories: memory organization			29.10.2020	28.10.2020				
45	6.7 and operation, expanding memory size,			02.11.2020	29.10.2020				
46	6.8 Classification and characteristics of memories, RAM ROM, EPROM, EEPROM, NVRAM, SRAM, and DRAM.			04.11.2020	02.11.2020				
	Class Test III			09.12.2020	09.12.2020				

Start of Semester

Signature	Date
Course Faculty : <i>P. Sathya</i>	01/07/2020
HoD : <i>P. Sathya</i>	11/7/20

End of Semester

Signature	Date
Course Faculty : <i>P. Sathya</i>	30/11/2020
HoD : <i>P. Sathya</i>	8/12/20

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
Academic Year : 2020-21, Semester - I

PRACTICAL TEACHING RECORD

Subject Code : 204186		Class:SE	Name of Faculty: Anjali Jagtap			Pr : 2 Hrs / week	
Subject Name: Digital Circuits Lab.			Batch: A	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by
Sr. No.	Experiment / Assignment	CEO	CO				AC HOD APMC
1	Study of IC-74LS153 as a Multiplexer. (Refer Data-Sheet). a. Design and Implement 8:1 MUX using IC-74LS153 & Verify its Truth Table. b. Design & Implement the given 4 variable function using IC-74LS153. Verify its Truth-Table.	CEO207.1 CEO207.2 CEO207.3	CO207.1	24.07.2020	24.07.2020		✓
2	Study of IC-74LS138 as a Demultiplexer / Decoder. (Refer Data-Sheet) a. Design and Implement full adder and subtractor function using IC-74LS138. b. Design & Implement 3-bit code converter using IC-74LS138. (Gray to Binary/Binary to Gray).	CEO207.1 CEO207.2 CEO207.3	CO207.1	31.07.2020	31.07.2020		✓
3	Study of IC-74LS83 as a BCD adder. (Refer Data-Sheet). a. Design and Implement 1-digit BCD adder using IC-74LS83. b. Design and Implement 4-bit Binary subtractor using IC-74LS83.	CEO207.1 CEO207.2 CEO207.3	CO207.2	07.08.2020	07.08.2020		✓
4	Study of IC-74LS85 as a magnitude comparator. (Refer Data-Sheet) a. Design and Implement 4-bit Comparator. b. Design and Implement 8-bit Comparator.	CEO207.1 CEO207.2 CEO207.3	CO207.2	14.08.2020	28.08.2020		✓
5	Study of Counters: a. Design and Implement 4-bit counter using JK- Flip flop.	CEO207.1 CEO207.2 CEP207.4	CO207.3	21.08.2020	01.09.2020		✓
6	Study of Counter ICs (74LS90/74LS93). (Refer Data-Sheet) a. Design and Implement MOD-N and MOD-NN using IC-74LS90 and draw Timing diagram. b. Design and Implement MOD-N and MOD-NN using IC-74LS93 and draw Timing diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3	28.08.2020	09.09.2020		✓
7	Study of synchronous counter: a. Design & Implement 4-bit Up/down Counter and MOD-N Up/down Counter using IC-74HC191 / IC-74HC193. Draw Timing Diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3	04.09.2020	11.09.2020		✓
8	Verify four voltage and current parameters for TTL and CMOS (IC 74LSXX, 74HCXX). (Refer Data-Sheet).	CEO207.1 CEO207.2	CO207.4	11.09.2020	17.09.2020		✓

Sr. No.	Experiment / Assignment	CEO	CO	L. of Plan	Date of Conduction	Remarks of Faculty (Incase of variance)	Monitored by		
							AC	HOD	APMC
9	Study of Shift Register: Design and Implement 4-bit right shift and left shift register using D-flip flop.	CEO207.1 CEO207.2 CEO207.4	CO207.5	18.09.2020	18.09.2020		✓	✓	✓
10	Study of Shift Register (74HC194 / 74LS95): a. Design and Implement Pulse train generator using IC-74HC194 / IC74LS95 (Use right shift/ left shift). b. Design and Implement 4-bit Ring Counter/ Twisted ring Counter using shift registers IC 74HC194 / IC74LS95.	CEO207.1 CEO207.2 CEO207.4	CO207.5	25.09.2020	25.09.2020		✓	✓	✓
11	Study of code converter using virtual labs.	CEO207.1 CEO207.2 CEO207.4	CO207.6	09.10.2020	09.10.2020		✓	✓	✓
12	Study and Verify the truth table of RS, JK, T and D flip-flops using NAND gates.	CEO207.1 CEO207.2 CEO207.4	CO207.6	16.10.2020	16.10.2020		✓	✓	✓

Start of Semester

Signature	Date
Course Faculty: <i>P. Jeyaraj</i>	11/9/2020
HOD: <i>P. Jeyaraj</i>	11/9/20

End of Semester

Signature	Date
Course Faculty: <i>P. Jeyaraj</i>	30/11/2020
HOD: <i>P. Jeyaraj</i>	30/11/20



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
 Academic Year : 2020-21, Semester - I

PRACTICAL TEACHING RECORD

Subject Code : 204186		Class:SE	Name of Faculty: Anjali Jagtap		Teaching Scheme:		Pr : 2 Hrs / week	
Subject Name: Digital Circuits Lab.			Batch: B	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	AC	HOD
Sr. No.	Experiment / Assignment	CEO	CO					APMC
1	Study of IC-74LS153 as a Multiplexer. (Refer Data-Sheet). a. Design and Implement 8:1 MUX using IC-74LS153 & Verify its Truth Table. b. Design & Implement the given 4 variable function using IC-74LS153. Verify its Truth-Table.	CEO207.1 CEO207.2 CEO207.3	CO207.1	23.07.2020	23.07.2020		↑	↑
2	Study of IC-74LS138 as a Demultiplexer / Decoder. (Refer Data-Sheet) a. Design and Implement full adder and subtractor function using IC-74LS138. b. Design & Implement 3-bit code converter using IC-74LS138. (Gray to Binary/Binary to Gray).	CEO207.1 CEO207.2 CEO207.3	CO207.1	30.07.2020	30.07.2020		↑	↑
3	Study of IC-74LS83 as a BCD adder. (Refer Data-Sheet). a. Design and Implement 1-digit BCD adder using IC-74LS83. b. Design and Implement 4-bit Binary subtractor using IC-74LS83.	CEO207.1 CEO207.2 CEO207.3	CO207.2	06.08.2020	06.08.2020		↑	↑
4	Study of IC-74LS85 as a magnitude comparator. (Refer Data-Sheet) a. Design and Implement 4-bit Comparator. b. Design and Implement 8-bit Comparator.	CEO207.1 CEO207.2 CEO207.3	CO207.2	13.08.2020	27.08.2020		↑	↑
5	Study of Counters: a. Design and Implement 4-bit counter using JK-Flip flop.	CEO207.1 CEO207.2 CEO207.4	CO207.3	20.08.2020	03.09.2020		↑	↑
6	Study of Counter ICs (74LS90/74LS93): (Refer Data-Sheet) a. Design and Implement MOD-N and MOD-NN using IC-74LS90 and draw Timing diagram. b. Design and Implement MOD-N and MOD-NN using IC-74LS93 and draw Timing diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3	27.08.2020	09.09.2020		↑	↑
7	Study of synchronous counter: a. Design & Implement 4-bit Up/down Counter and MOD-N Up/down Counter using IC-74HC191 / IC-74HC193. Draw Timing Diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3	03.09.2020	17.09.2020		↑	↑
8	Verify four voltage and current parameters for TTL and CMOS (IC 74LSXX, 74HCXX). (Refer Data-Sheet).	CEO207.1 CEO207.2	CO207.4	10.09.2020	27.09.2020		↑	↑

Sr. No.	Experiment / Assignment	CEO	CO	1 of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
9	Study of Shift Register: Design and Implement 4 bit right shift and left shift register using D-flip flop.	CEO207.1 CEO207.2 CEO207.4	CO207.5	17.09.2020	01.10.2020		↑	↑	↑
10	Study of Shift Register (74HC194 / 74LS95): a. Design and Implement Pulse train generator using IC-74HC194 / IC74LS95 (Use right shift/ left shift), b. Design and Implement 4-bit Ring Counter/ Twisted ring Counter using shift registers IC 74HC194 / IC74LS95.	CEO207.1 CEO207.2 CEO207.4	CO207.5	24.09.2020	08.10.2020		↑	↑	↑
11	Study of code converter using virtual labs.	CEO207.1 CEO207.2 CEO207.3	CO207.6	01.10.2020	15.10.2020		↑	↑	↑
12	Study and Verify the truth table of RS, JK, T and D flip-flops using NAND gates.	CEO207.1 CEO207.2 CEO207.4	CO207.6	08.10.2020	22.10.2020		↑	↑	↑

Start of Semester

Signature	Date
<i>[Signature]</i>	17/10/2020
Course Faculty:	
HOD:	17/10/2020

End of Semester

Signature	Date
<i>[Signature]</i>	30/11/2020
Course Faculty:	
HOD:	30/11/2020

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
 Academic Year : 2020-21, Semester - I

PRACTICAL TEACHING RECORD

Subject Code : 204186			Class:SE		Name of Faculty: Anjali Jagtap		Teaching Scheme:		Pr : 2 Hrs / week		
Subject Name: Digital Circuits Lab.					Batch: C	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	AC	HOD	APMC
Sr. No.	Experiment / Assignment	CEO	CO								
1	Study of IC-74LS153 as a Multiplexer. (Refer Data-Sheet). a. Design and Implement 8:1 MUX using IC-74LS153 & Verify its Truth Table. b. Design & Implement the given 4 variable function using IC74LS153. Verify its Truth-Table.	CEO207.1 CEO207.2 CEO207.3	CO207.1		29.07.2020	22.07.2020					
2	Study of IC-74LS138 as a Demultiplexer / Decoder. (Refer Data-Sheet) a. Design and Implement full adder and subtractor function using IC-74LS138. b. Design & Implement 3-bit code converter using IC-74LS138. (Gray to Binary/Binary to Gray).	CEO207.1 CEO207.2 CEO207.3	CO207.1		05.08.2020	29.07.2020					
3	Study of IC-74LS83 as a BCD adder. (Refer Data-Sheet). a. Design and Implement 1-digit BCD adder using IC-74LS83. b. Design and Implement 4-bit Binary subtractor using IC-74LS83.	CEO207.1 CEO207.2 CEO207.3	CO207.2		12.08.2020	05.08.2020					
4	Study of IC-74LS85 as a magnitude comparator. (Refer Data-Sheet) a. Design and Implement 4-bit Comparator. b. Design and Implement 8-bit Comparator.	CEO207.1 CEO207.2 CEO207.3	CO207.2		19.08.2020	26.08.2020					
5	Study of Counters: a. Design and Implement 4-bit counter using JK- Flip flop.	CEO207.1 CEO207.2 CEO207.4	CO207.3		26.08.2020	02.09.2020					
6	Study of Counter ICs (74LS90/74LS93): (Refer Data-Sheet) a. Design and Implement MOD-N and MOD-NN using IC-74LS90 and draw Timing diagram. b. Design and Implement MOD-N and MOD-NN using IC-74LS93 and draw Timing diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3		02.09.2020	09.09.2020					
7	Study of synchronous counter: a. Design & Implement 4-bit Up/down Counter and MOD-N Up/down Counter using IC74HC191 / IC74HC193. Draw Timing Diagram.	CEO207.1 CEO207.2 CEO207.4	CO207.3		09.09.2020	11.09.2020					
8	Verify four voltage and current parameters for TTL and CMOS (IC 74LSXX, 74HCXX). (Refer Data-Sheet).	CEO207.1 CEO207.2	CO207.4		16.09.2020	16.09.2020					

Sr. No.	Experiment / Assignment	CEO	CO	1. of Plan	Date of Conduction	Remarks of Faculty (Incase of variance)	Monitored by		
							AC	HOD	APMC
9	Study of Shift Register: Design and Implement 4-bit right shift and left shift register using D-flip flop.	CEO207.1 CEO207.2 CEO207.4	CO207.5	23.09.2020	23.09.2020		✓	✓	✓
10	Study of Shift Register (74HC194 / 74LS95): a. Design and Implement Pulse train generator using IC-74HC194 / IC74LS95 (Use right shift/ left shift). b. Design and Implement 4-bit Ring Counter/ Twisted ring Counter using shift registers IC 74HC194 / IC74LS95.	CEO207.1 CEO207.2 CEO207.4	CO207.5	30.09.2020	01.10.2020		✓	✓	✓
11	Study of code converter using virtual labs.	CEO207.1 CEO207.2 CEO207.3	CO207.5	07.10.2020	07.10.2020		✓	✓	✓
12	Study and Verify the truth table of RS, JK, T and D flip-flops using NAND gates.	CEO207.1 CEO207.2 CEO207.4	CO207.6	14.10.2020	14.10.2020		✓	✓	✓

Start of Semester

Signature	Date
<i>[Signature]</i>	11/9/2020
Course Faculty :	
HOD :	

End of Semester

Signature	Date
<i>[Signature]</i>	30/11/2020
Course Faculty :	
HOD :	



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DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION

I2IT / ACAD / CP / 05 Ve

Academic Year : 2020-21, SEM - I

Class: SE	Course: Digital Circuits
Course Code: 204183	Faculty Name: Anjali A Jagtap

LESSON PLAN & RESOURCES

Sr. No	Topics to be covered	References (With Page No)
1	Unit 1: Digital Logic Families	T1: Pg. No. 89-126
2	Unit 2: Combinational Logic Design	T1 Pg. No. 166-178
3	Unit 3: Combinational Circuits	T1 Pg. No. 191 - 234
4	Unit 4: Sequential Logic Design	T1: Pg. No. 237- 306
5	Unit 5: State Machines	R1: Pg. No. 699 -744, 745- 798
6	Unit 6: Programmable Logic Devices	T1 Pg. No. 463-515 R1 Pg. No. 390-416, 810-820

REFERENCES

A. TEXTBOOKS

Sr.No.	Book Name	Authors
T1	Modern digital electronics, 3rd edition, 12th reprint Tata McGraw Hill Publication, 2007	R.P. Jain
T2	Digital logic design and Computer Design, 4th edition, Prentice Hall of India, 2013.	M Morris Meno

B. REFERENCE BOOKS

Sr.No.	Book Name	Authors
R1	Fundamentals of digital circuits, 1st edition, Prentice Hall of India, 2001	A. Anand Kumar
R2	Programming and customizing the 8051 microcontroller, Tata McGraw Hills, 2003	Myke Predko
R3	The 8051 Microcontroller and Embedded Systems using Assembly and C, 2nd edition, Pearson education.	Muhammad Mazidi, Janice Mazidi and Rolin McKinlay

C. DIGITAL CONTENTS

Sr.No.	Web Address
W1	https://nptel.ac.in/courses/108/105/108105113
W2	https://nptel.ac.in/courses/117/106/117106086/
W3	https://nptel.ac.in/courses/108/105/108105132/

DETAILS OF LMS OF THE COURSE

<https://classroom.google.com/c/NTQ1MTU2NzIyMDha>

Anjali A Jagtap
 Signature of Faculty

R. Hatake
 HoD



**HOPE FOUNDATION'S
INTERNATIONAL INSTITUTE OF INFORMATION TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
ACADEMIC YEAR : 2020-21, SEM - I**

I2IT / ACAD / CP / 06 Ver

Course : Digital Circuits Lab.	Course Code :204186
Name of Faculty: Anjali A Jagtap	Class : SE

Rubrics for All Experiments

Performance Criterion (PC)	Excellent (3)	Good (2)	Poor (1)	Unacceptable (0)
Design (PC1)	Understanding of problem statement, accurate listing of input and output, formation of truth table, accurate design steps, and circuit/logic diagram with desired IC.	Understanding of problem statement, accurate listing of input and output, formation of truth table, accurate design steps, but small mistake in circuit/logic diagram with desired IC.	Understanding of problem statement, accurate listing of input and output, formation of truth table, wrong design steps and no circuit/logic diagram with desired IC.	Understanding of problem statement, accurate listing of input and output, wrong truth table and hence wrong design.
Implementation (PC2)	Can do IC verification. Correct power supply connections. Accurate wiring connections as per circuit design.	Can do IC verification. Correct power supply connections. A small mistake in wiring connections as per circuit diagram but can identify.	Can do IC testing. Correct power supply connections. Few wrong wiring connections. Needs help to correct wiring connections.	Can not test IC. Incorrect supply connections. wrong wiring connections as per circuit diagram.
Output (PC3)	Result is correct. Complete truth table is verified with explanation.	Result is correct. But partial verification of truth table with explanation.	Result is correct. But partial verification of truth table but can not explanation.	Wrong result. Cannot verify truth table.


Signature of Faculty


HoD

COURSE FILE INDEX

S. No.	Format	DISCRIPTION
1	Faculty wise Time Table	I2IT / ACAD / TT / 02
2	Course Objectives and Outcomes (Theory)	I2IT / ACAD / CP / 01
3	Correlation of COs with POs (Theory)	I2IT / ACAD / CP / 02
4	Course Objectives and Outcomes (Laboratory)	I2IT / ACAD / CP / 01
5	Correlation of COs with POs ((Laboratory)	I2IT / ACAD / CP / 02
6	University Syllabus	
7	Previous University Question Papers	
8	Theory Question Bank	
9	Objective Question Bank	
10	Theory Teaching Plan	I2IT / ACAD / CP / 03
11	Laboratory Teaching Plan	I2IT / ACAD / CP / 04
12	Lesson Plan and Resources	I2IT / ACAD / CP / 05
13	Rubrics for Continuous evaluation	I2IT / ACAD / CP / 06
14	Class Test Question Papers with solutions	I2IT / ACAD / CP / 07
15	Class Test Attendance	I2IT / ACAD / CP / 08
16	Slow Learner and advanced Learner Identification	I2IT / ACAD / CP / 09
17	Schedule of slow Learner Activities	I2IT / ACAD / CP / 10
18	Assignments to Advanced Learners	I2IT / ACAD / CP / 11
19	Class Test Evaluation Record	I2IT / ACAD / CP / 12
20	List of Slow Learners	I2IT / ACAD / CP / 13
21	List of Advanced Learners	I2IT / ACAD / CP / 14
22	Slow Learner Attendance Record	I2IT / ACAD / CP / 15
23	Performance Improvement of Slow Learner	I2IT / ACAD / CP / 16
24	Content Beyond Syllabus	I2IT / ACAD / CP / 17
25	Theory Attendance Record	I2IT / ACAD / BB / 01
26	Practical Attendance Record	I2IT / ACAD / BB / 02
27	Continuous Assessment Record	I2IT / ACAD / BB / 03
28	Average University Result	I2IT / ACAD / AT / 01
29	CO Attainment through University Result	I2IT / ACAD / AT / 02
30	CO Attainment through Class Tests	I2IT / ACAD / AT / 03
31	CO Attainment through Course End Survey	I2IT / ACAD / AT / 04
32	CO Attainment through Continuous Evaluation	I2IT / ACAD / AT / 05
33	PO & PSO Attainment through CO for Theory	I2IT / ACAD / AT / 06
34	PO & PSO Attainment through CO for Practical	I2IT / ACAD / AT / 06
35	TW Calculation Sheet	I2IT / ACAD / AT / 07



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester - II

Class: T.E.	Course: Power Electronics
Course Code: 304186	Faculty Name: Dr. Varsha Degaonkar

COURSE EDUCATIONAL OBJECTIVES

Course Educational Objectives	Description
CEO309.1	To introduce students to different power devices to study their construction, characteristics and turning on circuits.
CEO309.2	To give an exposure to students of working & analysis of controlled rectifiers for different loads, inverters, DC choppers.
CEO309.3	To give an exposure to students of working & analysis of AC voltage controllers and resonant converters.
CEO309.4	To study the different motor drives, various power electronics applications like UPS, SMPS, etc.
CEO309.5	To study some protection circuits.

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO309.1	Explain working of power devices with gate drive circuits.
CO309.2	Perform & analyze AC to DC controlled power converters.
CO309.3	Perform & analyze DC to AC controlled power converters.
CO309.4	Perform & analyze DC to DC controlled power converters.
CO309.5	Explain Resonant Converters & protection of Power Circuits.
CO309.6	Explain working principle of electronic applications using power devices.

CLASS TEST- COURSE OUTCOME MAPPING

Question No	CLASS TEST - I		CLASS TEST-II		CLASS TEST - III	
	CO Mapping	Marks	CO Mapping	Marks	CO Mapping	Marks
Q1 - Q15	CO309.1	15	CO309.3	15	CO309.5	15
Q16 - Q30	CO309.2	15	CO309.4	15	CO309.6	15



Hope Foundation's
International Institute of Information Technology, Pune
Department of Electronics & Telecommunication
ACADEMIC YEAR : 2020 - 21, SEM - II

CORRELATION BETWEEN CO, PO & PSO

Course Name: Power Electronics	Course Code: 304186
Faculty Name: Dr. Varsha Degaonkar	Class: T.E.

CO-PO MATRIX

Course Outcome (COs)	Program Outcomes (POs)											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO309.1	2	-	-	-	-	-	-	-	-	-	-	-
CO309.2	2	1	1	-	-	-	-	-	-	-	-	-
CO309.3	2	1	1	-	-	-	-	-	-	-	-	-
CO309.4	2	1	1	-	-	-	-	-	-	-	-	-
CO309.5	2	-	-	-	-	-	-	-	-	-	-	-
CO309.6	2	1	1	-	-	-	-	-	-	-	-	-

1 - LOW, 2 - MEDIUM, 3 - HIGH

Outcome (Cos)	Program Specific Outcomes (PSOs)			
	PSO1	PSO2	PSO3	PSO4
CO309.1	1	-	-	-
CO309.2	2	-	-	-
CO309.3	2	-	-	-
CO309.4	2	-	-	-
CO309.5	2	-	-	-
CO309.6	2	-	-	1


Dr. Varsha Degaonkar
Signature of Faculty


Dr. Risil Chhatrala
Head of Department



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester - II

Class: T.E.	Course: Power and ITCT Lab
Course Code: 304194	Faculty Name: Prof. Varsha D., Prof. Smita Kadam

COURSE EDUCATIONAL OBJECTIVES

Course Outcome	Description
CEO314.1	To introduce students to different power devices to study their construction, characteristics and turning on circuits.
CEO314.2	To give an exposure to students of working & analysis of controlled rectifiers for different loads, inverters, DC choppers and AC voltage controllers.
CEO314.3	To study the different motor drives, various power electronics applications like SMPS, etc.
CEO314.4	To understand information theoretic behavior of a communication system.
CEO314.5	To understand various source & channel coding techniques for data compression
CEO314.6	To Build an understanding of fundamental concepts of data communication and networking

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO314.1	Design & implement a triggering / gate drive circuit for a power device.
CO314.2	Perform & analyze different controlled converters.
CO314.3	Evaluate performance of various motors and SMPS
CO314.4	Perform information theoretic analysis of communication system.
CO314.5	Design a source & channel coding scheme for a communication system.
CO314.6	Apply fundamental principles of data communication and networking

ASSIGNMENT- COURSE OUTCOME MAPPING

Assignment	Course Outcome No	Assignment	Course Outcome No
1	CO314.1	9	CO314.4
2		10	CO314.5
3		11	
4	CO314.2	12	
5		13	
7		15	CO314.6
6	CO314.3	14	
8		16	

Sign of Faculty 



Hope Foundation's
International Institute of Information Technology, Pune
Department of Electronics & Telecommunication
ACADEMIC YEAR : 2020 - 21, SEM - II

CORRELATION BETWEEN CO, PO & PSO

Course Name: Power and ITCT Lab	Course Code: 304194
Faculty Name: Prof. Varsha D., Prof. Smita Kadam	Class: T.E.

CO-PO MATRIX

Course Outcome (COs)	Program Outcomes (POs)											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO314.1	2	-	1	-	-	-	-	-	-	-	-	-
CO314.2	2	-	1	-	-	-	-	-	-	-	-	-
CO314.3	2	-	1	-	-	-	-	-	-	-	-	-
CO314.4	2	2	1	1	-	1	-	-	-	1	-	-
CO314.5	2	2	1	1	-	1	-	-	-	1	-	-
CO314.6	2	2	1	1	-	1	-	-	-	1	-	-

1 - LOW, 2 - MEDIUM, 3 - HIGH

Outcome (Cos)	Program Specific Outcomes (PSOs)			
	PSO1	PSO2	PSO3	PSO4
CO314.1	2	-	-	-
CO314.2	2	-	-	-
CO314.3	2	-	-	-
CO314.4	2	2	1	-
CO314.5	2	2	1	-
CO314.6	2	2	1	-

Varsha D.
 Prof. Varsha D., Prof. Smita Kadam
 Signature of Faculty

R. Chhatrala
 Dr. Risil Chhatrala
 Head of Department

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year: 2020-21, Semester - II

THEORY TEACHING RECORD

Course Code : 304186				Class: T. E.				Name of Faculty: Dr. Varsha Degaonkar				
Course Name: Power Electronics								Teaching Scheme:		Th : 3 Hrs / week		
Lr. No.	Topics to be Delivered			CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC	HOD	APMC	
UNIT 1 : Power Devices												
1	1.1	Introduction of power electronics, Construction, Steady state & Switching characteristics of SCR	CEO309.1	CO309.1	05-01-2021	05-01-2021						
2	1.2	Construction, Steady state characteristics of Power MOSFET & IGBT.			06-01-2021	06-01-2021						
3	1.3	SCR ratings: I_L , I_H , V_{BO} , V_{BR} , dv/dt , di/dt , surge current & rated current.			07-01-2021	27-01-2021		ML (rescheduled)				
4	1.4	Gate characteristics, Gate drive requirements, Gate drive circuits for Power MOSFET & IGBT,			12-01-2021	27-01-2021		ML (rescheduled)				
5	1.5	Opto-isolator driving circuits for SCR. Series and parallel operations of SCR's.			13-01-2021	02-02-2021		GTT Barclays session				
6	1.6	Applications of above power devices as a switch			14-01-2021	02-02-2021		GTT Barclays session				
UNIT 2 : AC-DC Power Converters												
7	2.1	Introduction to phase controlled Rectifiers, Concept of line & forced commutation, Single phase Semi converters for R load			19-01-2021	03-02-2021		GTT Barclays session				
8	2.2	Single phase Semi converters for R-L load, Comparison of semi converters, Single phase Full converters for R & R-L load.			20-01-2021	04-02-2021		Add-On course				
9	2.3	Performance parameters, Effect of freewheeling diode.			21-01-2021	05-02-2021		Add-On course				
10	2.4	Three phase basics. Three phase Semi & Full converters for R load.	CEO309.2	CO309.2	27-01-2021	05-02-2021		Adjusted session of BM				
11	2.5	Three phase Semi & Full converters for R load. Effect of source inductance, Power factor improvement techniques,			28-01-2021	09-02-2021		Add-On course				
12	2.6	Diode based boost converter, Single Phase dual converter with inductive load.			02-02-2021	09-02-2021						

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by			
UNIT 3 : DC-AC Converters								AC	HOD	APMC
13	3.1 Single phase bridge inverter for R load using MOSFET / IGBT,	CEO309.2	CO309.3	03-02-2021	09-02-2021					
14	3.2 Single phase bridge inverter for R-L load using MOSFET / IGBT			04-02-2021	10-02-2021					
15	3.3 performance Parameters, Single phase PWM inverters.			09-02-2021	11-02-2021					
16	3.4 3 Phase voltage source inverter for balanced star R load with 120° mode of operation,			10-02-2021	12-02-2021					
17	3.5 3 Phase voltage source inverter for balanced star R load with 180° mode of operation,			11-02-2021	18-02-2021					
18	3.6 Device utilization factor, Harmonics Elimination / Modulation Techniques.			16-02-2021	23-02-2021	Taken by Smita K				
UNIT 4 : DC-DC converters & AC Voltage Controller										
19	4.1 Introduction to dc – dc converter, Working principle of step down chopper for R-L load (highly inductive),	CEO309.2, CEO309.3	CO309.4	17-02-2021	24-02-2021	Rescheduled on 18 Feb				
20	4.2 Control strategies, Step down chopper for R-L load, Performance parameters,			23-02-2021	25-02-2021					
21	4.3 Step up chopper, 2-quadrant choppers,			24-02-2021	02-03-2021					
22	4.4 4-quadrant choppers			25-02-2021	03-03-2021					
23	4.5 SMPS: Fly back/ Half Bridge/ LM3524 based or equivalent Circuit.			02-03-2021	04-03-2021					
24	4.6 Introduction to ac – ac converter, Single-phase full wave AC voltage controller by using IGBT with R load.			03-03-2021	09-03-2021					

Lr. No.		Topics to be Delivered		CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	Monitored by			
		UNIT 5 : Resonant Converters & Protection of Power Devices & Circuits										
25	5.1	Importance and elements of Communication system Need for resonant converters, classification,		CEO309.3, CEO309.5	CO309.5	04-03-2021	16-03-2021	due to awards meeting, session on 10 march is rescheduled				
26	5.2	Concept of zero current switching (ZCS), zero voltage switching (ZVS) resonant converters.				09-03-2021 test 2	17-03-2021					
27	5.3	Cooling & heat sinks, over voltage conditions				10-03-2021	18-03-2021					
28	5.4	over voltage protection circuits, over volt. fault conditions, metal oxide varistors,				16-03-2021	23-03-2021					
29	5.5	over current protection, over current fault conditions				17-03-2021	24-03-2021					
30	5.6	Electromagnetic interference: Sources, Its minimizing techniques, Shielding techniques for EMI.				18-03-2021	25-03-2021					
UNIT 6 : Power Electronics Applications												
31	6.1	ON-line and OFF line UPS with battery AH, Back up time, battery charger rating		CEO309.4 CO309.6	CO309.6	23-03-2021	06-04-2021					
32	6.2	Electronic ballast, LED Lamp with Driver Circuit, fan Regulator.				24-03-2021	07-04-2021					
33	6.3	1 phase separately excited DC motor drive				25-03-2021	08-04-2021					
34	6.4	Construction, Working, types of stepper motor , its drive Construction,				30-03-2021	19-05-2021					
35	6.5	Working, Characteristics & applications of BLDC motor.				31-03-2021	20-05-2021					
36	6.6	Variable voltage & variable frequency three phase induction motor drive.				01-04-2021	24-05-2021 25-05-2021					

Start of Semester

Start of Semester

Signature	Date
Course Faculty : <i>Don</i>	4/1/21
HoD : <i>Phataala</i>	04/02/21

End of Semester

Signature	Date
Course Faculty : <i>Don</i>	30/6/21
HoD : <i>Phataala</i>	30/1/20

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester -II

PRACTICAL TEACHING RECORD

Subject Code : 304194		Class: T. E.		Name of Faculty: Dr. Varsha Degaonkar		Teaching Scheme:		Pr : 2 Hrs / week	
Subject Name: Power and ITCT Lab		Batch: A3E		Date of Plan		Date of Conduction		Remarks of Faculty (incase of variance)	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	AC	HOD	APMC	Monitored by
1	Characteristics of SCR	CEO314.1	CO314.1	15-02-2021	15-02-2021, 25-05-2021				
2	V-I Characteristics of MOSFET and IGBT	CEO314.1	CO314.1	17-02-2021	17-02-2021				
3	Single phase Full Converter with R load	CEO314.2	CO314.2	01-03-2021	01-03-2021				
4	Three phase Full Converter with R load	CEO314.2	CO314.2	08-03-2021	08-03-2021				
5	Single phase AC voltage controller using SCRs for R load	CEO314.2	CO314.2	15-03-2021	15-03-2021				
6	Single-Phase PWM bridge inverter for R loads Observe output RMS voltage	CEO314.3	CO314.3	22-03-2021	22-03-2021				
7	Find load & line regulation of given SMPS. Multisim->Samples->Power Conversion-> DC-DC-> Push-Pull Isolated Buck Converter	CEO314.2	CO314.2	05-04-2021	05-04-2021				
8	Speed control of DC motor using armature voltage control / field control method. (https://ems-iitr.vlabs.ac.in/exp/dcshunt-motor-armature-control/)	CEO314.3	CO314.3	18-05-2021	18-05-2021				

Start of Semester

Signature	Date
Course Faculty : <i>Dev</i>	4/1/21
HoD : <i>P. Hatawala</i>	4/1/21

End of Semester

Signature	Date
Course Faculty : <i>Dev</i>	30/6/21
HoD : <i>P. Hatawala</i>	30/6/21

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester -II

PRACTICAL TEACHING RECORD

Subject Code : 304194		Class: T. E.		Name of Faculty: Dr. Varsha Degaonkar		Teaching Scheme:		Pr : 2 Hrs / week	
Subject Name: Power and ITCT Lab		Batch: B3E		Date of Plan		Date of Conduction		Remarks of Faculty (incase of variance)	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan		Date of Conduction		Monitored by	
								AC	HOD APMC
1	Characteristics of SCR	CEO314.1	CO314.1	15-02-2021		15-02-2021, 25-05-2021			
2	V-I Characteristics of MOSFET and IGBT	CEO314.1	CO314.1	17-02-2021		17-02-2021			
3	Single phase Full Converter with R load	CEO314.2	CO314.2	01-03-2021		01-03-2021			
4	Three phase Full Converter with R load	CEO314.2	CO314.2	08-03-2021		08-03-2021			
5	Single phase AC voltage controller using SCRs for R load	CEO314.2	CO314.2	15-03-2021		15-03-2021			
6	Single-Phase PWM bridge inverter for R loads Observe output RMS voltage	CEO314.3	CO314.3	22-03-2021		22-03-2021			
7	Find load & line regulation of given SMPS. Multisim->Samples->Power Conversion-> DC-DC-> Push-Pull Isolated Buck Converter	CEO314.2	CO314.2	05-04-2021		05-04-2021			
8	Speed control of DC motor using armature voltage control / field control method. (https://ems-iitr.vlabs.ac.in/exp/dcs-hunt-motor-armature-control/)	CEO314.3	CO314.3	18-05-2021		18-05-2021			

Start of Semester

Signature	Date
Course Faculty : <i>Don</i>	4/1/21
HoD :	2/1/21

End of Semester

Signature	Date
Course Faculty : <i>Don</i>	5/6/21
HoD :	30/6/21

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester -II

PRACTICAL TEACHING RECORD

Subject Code : 304194		Class: T. E.		Name of Faculty: Dr. Varsha Degaonkar		Teaching Scheme:		Pr : 2 Hrs / week	
Subject Name: Power and ITCT Lab		Experiment / Assignment		Batch: C3E		Date of Conduction		Remarks of Faculty (incase of variance)	
Sr. No.	CO	CEO	CO	Date of Plan	Date of Conduction	AC	HOD	APMC	Monitored by
1	CO314.1	CEO314.1	CO314.1	15-02-2021	15-02-2021, 25-05-2021				
2	CO314.1	CEO314.1	CO314.1	17-02-2021	17-02-2021				
3	CO314.2	CEO314.2	CO314.2	01-03-2021	01-03-2021				
4	CO314.2	CEO314.2	CO314.2	08-03-2021	08-03-2021				
5	CO314.2	CEO314.2	CO314.2	15-03-2021	15-03-2021				
6	CO314.3	CEO314.3	CO314.3	22-03-2021	22-03-2021				
7	CO314.2	CEO314.2	CO314.2	05-04-2021	05-04-2021				
8	CO314.3	CEO314.3	CO314.3	18-05-2021	18-05-2021				

Start of Semester

Signature	Date
Course Faculty : <i>Dev</i>	4/1/21
HoD : <i>Phatake</i>	4/1/21

End of Semester

Signature	Date
Course Faculty : <i>Dev</i>	30/6/21
HoD : <i>Phatake</i>	30/6/21

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, Semester -II

PRACTICAL TEACHING RECORD

Subject Code : 304194		Class: T. E.		Name of Faculty: Dr. Varsha Degaonkar		Teaching Scheme:		Pr : 2 Hrs / week	
Subject Name: Power and ITCT Lab		Experiment / Assignment		Batch: D3E		Date of Conduction		Remarks of Faculty (in case of variance)	
Sr. No.	CO	CEO	CO	Date of Plan	Date of Conduction	Monitored by		AC	APMC
1	CO314.1	CEO314.1	CO314.1	15-02-2021	15-02-2021, 25-05-2021				
2	CO314.1	CEO314.1	CO314.1	17-02-2021	17-02-2021				
3	CO314.2	CEO314.2	CO314.2	01-03-2021	01-03-2021				
4	CO314.2	CEO314.2	CO314.2	08-03-2021	08-03-2021				
5	CO314.2	CEO314.2	CO314.2	15-03-2021	15-03-2021				
6	CO314.3	CEO314.3	CO314.3	22-03-2021	22-03-2021				
7	CO314.2	CEO314.2	CO314.2	05-04-2021	05-04-2021				
8	CO314.3	CEO314.3	CO314.3	18-05-2021	18-05-2021				

Start of Semester

Signature	Date
Course Faculty : <i>DDN</i>	4/1/21
HoD :	4/1/21

End of Semester

Signature	Date
Course Faculty : <i>DDN</i>	30/6/21
HoD :	30/6/21

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
Academic Year : 2020 - 21, SEM - II

Class: T. E.	Course: Power Electronics
Course Code: 304186	Faculty Name: Dr. Varsha Degaonkar

LESSON PLAN & RESOURCES

Sr.No.	Topics to be covered	References (With Page No)
1	Unit 1	T1(1-2), T2(81-84), R5(38-41,133-163,218-225), R3(20-100), W1-W4
2	Unit 2	R5(259-61, 272-78, 311-13), R2(15-97), T1(474-89), W1-W4
3	Unit 3	R5(454-459, 488-498, 510-516), T1(230-31,237-247, 280-284), W1-W4
4	Unit 4	R5(380-96), T1(166-168, 176-181, 500-508), T2(402-403), W1-W4
5	Unit 5	R1(258-264, 273-280, 368-370, 500-502, 717,737), T1(388-396), W1-W4
6	Unit 6	T2(396-402), R1(452-54), R4(40-78), T1(641-647), W1-W4

REFERENCES

A. TEXTBOOKS

Sr.No.	Book Name	Authors
T1	Power Electronics circuits devices and applications, PHI 3rd edition, 2004 edition, New Delhi.	M. H. Rashid
T2	Power Electronics, PHI, 2004, New Delhi ISBN 8120323963, 9788120323964	M. S.Jamil Asgar

B. REFERENCE BOOKS

Sr.No.	Book Name	Authors
R1	Power Electronics Converters Applications and Design, 2nd edition, John Wiley & sons, Singapore, Oxford University Press, New Delhi, 2005	Ned Mohan, T. Undeland & W. Robbins
R2	Modern Power Electronics, S Chand & Co New Delhi ISBN-10: 8121924251, ISBN-13: 978-8121924252	P.C. Sen
R3	GE SCR MANUAL, 6th edition, General Electric, New York, USA	--
R4	Power Electronics, Khanna Publishers, Delhi. 5th edition	Dr. P. S. Bimbhra
R5	Power Electronics, TMH 2nd edition	M D Singh, K B Khanchandani

C. DIGITAL CONTENTS

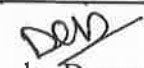
Sr.No.	Web Address
W1	https://ocw.mit.edu/courses/electrical-engineering-and-computer-science/6-334-power-electronics-spring-2007/
W2	https://www.electronics-tutorial.net/index.html , https://www.tutorialspoint.com/power_electronics/ https://www.pantechsolutions.net/powerelectronics-tutorials/
W3	https://www.electronics-project-design.com/ , http://www.circuitstoday.com/
W4	https://www.mindmeister.com/626402883/power-electronics-sources http://www.myelectrical2015.com/

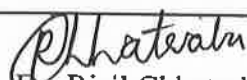
DETAILS OF LMS OF THE COURSE

<https://isquareit.akronsystems.com/ASSIGNMENT/pDOCUMENTUPLOAD.aspx?T=42&Msg=1&KEY=LR>

GOOGLE CLASSROOM OF THE COURSE

Code: rod2a5w


Dr. Varsha Degaonkar
Signature of Faculty


Dr. Risil Chhatrala
HOD

**HOPE FOUNDATION'S
INTERNATIONAL INSTITUTE OF INFORMATION TECHNOLOGY**

Department of Electronics & Telecommunication

ACADEMIC YEAR : 2020 - 21, SEM - II

Course : Power and ITCT Lab	Course Code : 304194
Name of Faculty: Dr. Varsha Degaonkar	Class : T.E.

Rubrics for all Power Electronics Lab Experiments

Performance	Excellent (3)	Good (2)	Poor (1)	Unacceptable (0)
Experimental Setup (PC1) The student identify and describes the lab components	The student identifies and describes all components	The student identifies and describes the common lab components	The student makes some mistakes while identifying and/or describing the common lab components	The student makes several mistakes while identifying and describing the common lab components
Measurement and data (PC2) The student performs measurements of different physical quantities.	The student performs correctly all measurements of different physical quantities and also proposes effective ways to better accomplish the task.	The student performs correctly measurements of different physical quantities.	The student performs measurements of physical quantities with some errors.	The student makes significant errors in measurement of physical quantities.
Student Attitude (PC3) The student applies rules of personal safety along experimental activities.	The student shows high responsibility along experimental activities by applying rules of personal safety.	The student applies rules of personal safety along experimental activities.	The student shows some distractions with respect to some rules of personal safety along experimental activities.	The student doesn't apply the rules of personal safety along experimental activities.



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY
Academic Year : 2020-21, Semester - II

Class: BE	Course: Internet & Web Programming Lab
Course Code: 414464D	Faculty Name: Prof. Manjusha Amritkar

COURSE EDUCATIONAL OBJECTIVES

Course Objective	Description
CEO412.1	To understand Web Programming basic concepts.
CEO412.2	To develop client side web programming skills.
CEO412.3	To develop server side web programming skills.
CEO412.4	To understand Web Services.
CEO412.5	To understand Content Management System.
CEO412.6	To understand Web Programming with database connectivity

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO412.1	Demonstrate static website using basic tools
CO412.2	Apply client side programming skills.
CO412.3	Implement server side programming skills.
CO412.4	Implement web service with suitable application
CO412.5	Develop website using content management tools.
CO412.6	Develop website using front end technology and server side technology

Sign of Faculty: _____



Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY
Academic Year : 2020-21, Semester - II

Class: BE	Course: Internet and Web Programming
Course Code: 414464D	Faculty Name: Manjusha Amritkar

COURSE EDUCATIONAL OBJECTIVES

Course Objective	Description
CEO412.1	To understand Internet and Web Programming basic concepts.
CEO412.2	To develop client side web programming skills.
CEO412.3	To develop server side web programming skills.
CEO412.4	To understand Web Services and Content Management System.
CEO412.5	To understand mobile web development and develop mobile web development skills.
CEO412.6	To understand web security and cyber ethics.

COURSE OUTCOME - DETAILS

Course Outcome	Description
CO412.1	Demonstrate static website using basic tools
CO412.2	Understand and apply client side programming skills.
CO412.3	Understand and implement server side programming skills.
CO412.4	Compare and analyze different web services and handle content management tools.
CO412.5	Construct mobile website using mobile web development tools.
CO412.6	Understand aspects of web security and cyber ethics.

CLASS TEST- COURSE OUTCOME MAPPING

Question No	CLASS TEST - I		CLASS TEST-II		CLASS TEST - III	
	CO Mapping	Marks	CO Mapping	Marks	CO Mapping	Marks
	CO412.1	15	CO412.3	15	CO412.5	15
	CO412.2	15	CO412.4	15	CO412.6	15

Signature of Faculty

Head of Department

Sign of Faculty: _____



Hope Foundation's
Department of INFORMATION TECHNOLOGY
ACADEMIC YEAR : 2020-21, SEM - II

CORRELATION BETWEEN CO, PO & PSO

Course Name: Internet and Web Programming	Course Code: 414464D
Faculty Name: Prof. Manjusha Amritkar	Class: BE

CO-PO MATRIX

Outcome (Cos)	Program Outcomes (POs)											
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO412.1	2	-	-	-	2	-	-	-	-	-	-	1
CO412.2	2	2	1	-	2	-	-	-	-	-	-	1
CO412.3	2	2	2	1	2	-	-	-	-	-	-	1
CO412.4	1	2	1	-	3	-	-	-	-	-	-	1
CO412.5	1	1	2	-	2	-	-	-	-	-	-	1
CO412.6	1	2	-	-	-	-	-	1	-	-	-	1
1 - LOW , 2 - MEDIUM , 3 - HIGH												

Outcome (Cos)	Program Specific Outcomes (PSOs)			
	PSO1	PSO2	PSO3	PSO4
CO412.1	1	0	1	0
CO412.2	1	0	1	0
CO412.3	1	2	1	0
CO412.4	1	0	1	0
CO412.5	1	0	1	0
CO412.6	0	0	1	0

Signature of Faculty

Head of Department

JUSTIFICATIONS FOR CO-PO/PSO MAPPING

Mapping	Low/Medium/High	Justification
CO412.1-PO1	Medium	Knowledge of HTML5, CSS and basic web related concepts will help the students to build static and dynamic webpages to give solutions for engineering problems.
CO412.1-PO5	Medium	Using modern tool students will build webpages which will be helpful to solve engineering problems.
CO412.1-PO12	Low	Knowledge of basic concepts of web development will be helpful in their future life at some extent.
CO412.1-PSO1	Low	Knowledge of basic concepts of web development will be helpful to solve real world problems
CO412.1-PSO3	Low	Knowledge of different design strategies like HTML, CSS and modern tool of web development will be helpful to deliver a quality product.
CO412.2-PO1	Medium	Knowledge of client side programming skill will help the students to build front end of website to give solutions for engineering problems.
CO412.2-PO2	Medium	Use of different front end technologies like HTML, CSS and Java script will help students to create website in an attractive manner
CO412.2-PO3	Low	Using different design alternatives like angular, JS can integrate modules and functionalities can be verified
CO412.2-PO5	Medium	Using modern tool students will build front end of webpage
CO412.2-PO12	Low	Knowledge of client side programming will help in web development in their future life
CO412.2-PSO1	Low	Knowledge of client side programming will be helpful to solve real world problems
CO412.2-PSO3	Low	Knowledge of different design strategies in client side programming and modern tool of web development will be helpful to deliver a quality product.

CO412.3-PO1	Medium	Knowledge of server side programming skill will help the students to build back end of website to give solutions for engineering problems.
CO412.3-PO2	Medium	Use of server side technologies like servlet, PHP, JSP will help students to compare alternative solutions to solve problems
CO412.3-PO3	Medium	Use of server side technologies like servlet, PHP, JSP will help students to connect with database
CO412.3-PO4	Low	Use of server side technologies like servlet, PHP, JSP will help students to connect with database and analyze the output at some extent
CO412.3-PO5	Medium	Using modern tool students will build website using front end and back end technologies with database connectivity
CO412.3-PO12	Low	Knowledge of server side programming will help in web development in their future life
CO412.3-PSO1	Low	Knowledge of server side programming will be helpful to solve real world problems
CO412.3-PSO2	Low	Knowledge of database connectivity will be helpful to solve social problems
CO412.3-PSO3	Low	Knowledge of different design strategies like servlet, JSP and PHP at server side programming and modern tool of web development will be helpful to deliver a quality product.
CO412.4-PO1	Low	Knowledge of web services will help students to build an application to give solutions for engineering problems.
CO412.4-PO2	Medium	Students can compare and select best CMS tool as per requirement
CO412.4-PO3	Low	Students can explore different CMS technologies to build website
CO412.4-PO5	High	Using modern tool like wordpress/Joomla students will build website.
CO412.4-PO12	Low	Knowledge of CMS will help in web development in their future life
CO412.4-PSO1	Low	Knowledge of web services will be helpful to solve real world problems
CO412.4-PSO3	Low	Knowledge of different web services and modern tool of web development will be helpful to deliver a quality product.

CO412.5-PO1	Low	Knowledge of mobile web development will help students to build an
CO412.5-PO2	Low	Students will be able to formulate module for mobile using jquery mobile
CO412.5-PO3	Medium	Students will be able to produce design solutions to meet functional
CO412.5-PO5	Medium	Using modern tool students will be able to design an application for mobile
CO412.5-PO12	Low	Knowledge of mobile web development will help to build an application in their future life
CO412.5-PSO1	Low	Knowledge of mobile web development will be helpful to solve real world
CO412.5-PSO3	Low	Knowledge of jquery mobile will be helpful to deliver a quality product.
CO412.6-PO1	Low	Knowledge of web security will help students to build secure web application.
CO412.6-PO2	Medium	Students can explore different level of security and can apply to build an application
CO412.6-PO8	Low	Students will understand the concepts of cyber ethics will help them to solve real world problems
CO412.6-PO12	Low	Knowledge of web security and ethics will help to build secure web application
CO412.6-PSO3	Low	Knowledge of web security will be helpful to deliver a quality product.

M.
Signature of Faculty

[Signature]
Head of Department

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY
Academic Year: 2020-21, Semester - II

THEORY TEACHING RECORD

Course Code : 414464D			Class: BE			Name of Faculty: Prof. Manjusha Amritkar						
Course Name: Internet and Web Programming						Teaching Scheme:				Th : 3 Hrs / week		
Lr. No.	Topics to be Delivered		CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)		AC	HOD	APMC	
UNIT 1 : INTERNET AND WEB PROGRAMMING ESSENTIALS												
1	1.1	The Internet, Introduction Basic Internet Protocol, The World Wide Web, Introduction to Web Programming, Web Clients, Web Servers, Browser and Search Engines	CEO412.1	CO412.1	4/1/2021	4/1/2021						
2	1.2	What happens when you type URL in browser? (beyond syllabus)			6/1/2021	6/1/2021						
3	1.3	Markup Languages : Introduction to HTML, Static and dynamic HTML, Structure of HTML documents, HTML Elements			7/1/2021	11/1/2021	Network Issue					
4	1.4	Linking in HTML, Anchor Attributes, Image Maps, Meta Information, Image Preliminaries			11/1/2021	12/1/2021						
5	1.5	Layouts, Backgrounds, Colors and Text, Fonts, Tables, Frames and layers, Audio and Video Support with HTML Database integration			12/1/2021	21/1/2021						
6	1.6	Forms Control, Form Elements, Applying Styles, values selectors, class, ids, inheritance, layout				25/1/2021	Barclays training from 14/1/2021 to 19/1/2021					
7	1.7	Usability Heuristics (beyond syllabus)				27/1/2021						
8	1.8	backgrounds, borders, margin, padding, lists, fonts, text formatting, positioning				1/2/2021						
9	1.9					2/2/2021						

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
10	1.1 HTML5: Introduction to Style Sheet, Inserting CSS in an HTML page, CSS selectors,			25/1/2021	8/2/2021				
11	1.1.1 Introduction to XML, XML key component, Transforming XML into XSLT			28/1/2021	10/2/2021				
12	1.1.2 DTD: Schema, elements, attributes, Introduction to JSON.			1/2/2021	15/2/2021				
UNIT 2 : CLIENT SIDE PROGRAMMING									
13	2.1 JavaScript: Overview of JavaScript, using JS in an HTML (Embedded, External), Data types, Control Structures			2/2/2021	17/2/2021				
14	2.2 Arrays, Functions and Scopes, Objects in JS			4/2/2021	22/2/2021				
15	2.3 DOM: DOM levels, DOM Objects and their properties and methods, Manipulating DOM,	CEO412.2	CO412.2	8/2/2021	1/3/2021				
16	2.4 JQuery: Introduction to JQuery,			11/2/2021	2/3/2021				
17	2.5 Introduction to AJAX, Working of AJAX, AJAX processing steps, coding AJAX script			15/2/2021	8/3/2021				
18	2.6 Introduction to Angular JS.			16/2/2021	10/3/2021				

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
UNIT 3 : SERVER SIDE PROGRAMMING									
19	3.1 Introduction to Server Side technology and TOMCAT			18/2/2021	15/3/2021				
20	3.2 Servlet: Introduction to Servlet, need and advantages, Servlet Lifecycle,			22/2/2021	17/3/2021				
21	3.3 Creating and testing of sample Servlet, session management			23/2/2021	5/4/2021				
22	3.4 JSP: Introduction to JSP, advantages of JSP over Servlet, elements of JSP page: directives, comments,	CEO412.3	CO412.3	1/3/2021	7/4/2021				
23	3.5 scripting elements, actions and templates, JDBC Connectivity with JSP.			2/3/2021	12/4/2021				
24	3.6 PHP: Introduction to PHP, Features, PHP script, PHP syntax, conditions & Loops, Functions, String manipulation, Arrays & Functions, Form handling, Cookies & Sessions, using MySQL with PHP			4/3/2021	12/5/2021				
UNIT 4 : WEB SERVICES AND CONTENT MANAGEMENT SYSTEMS									
25	4.1 Introduction to Web Services, Web Services Architecture			8/3/2021	26/4/2021				
26	4.2 XML Messaging, SOAP, WSDL, UDDI, REST,			15/3/2021	28/4/2021				
27	4.3 Java Web Services, Amazon Web Services, DevOps	CEO412.4	CO412.4	16/3/2021	3/5/2021				
28	4.4 Introduction to Content Management System (CMS), Wordpress / Joomla			18/3/2021	5/5/2021				
29	4.5 Advanced Technology: Bootstrap, JSF, Spring.			22/3/2021	10/5/2021	Semester I In Semester exam break			

Lr. No.	Topics to be Delivered	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (Incase of variance)	Monitored by	
UNIT 5 : MOBILE WEB DEVELOPMENT								
30	What is Mobile Web? Understanding Mobile Devices, Mobile Data Usage, Mobiles and Desktops	CEO412.5	CO412.5	23/3/2021	17/5/2021	Semester I In Semester exam break	AC	HOD APMC
31	Building an HTML page, Getting jQuery Mobile, Implementing jQuery Mobile			25/3/2021	19/5/2021			
32	Working with data attributes, Working with jQuery Mobile Pages, Enhancing Pages with Headers, Footers, and Toolbars			30/3/2021	22/5/2021			
33	Working with Lists, Building a Simple Mobile Website, Working with Forms and jQuery Mobile			1/4/2021				
34	Creating Modal Dialogs and Widgets, Creating Grids, Panels, and Other Widgets			5/4/2021	28/5/2021			
35	jQuery Mobile Configuration, Utilities, and JavaScript Methods; Working with Events.			6/4/2021				
UNIT 6 : WEB SECURITY AND CYBER ETHICS								
36	Overview of Web Security: Need of Web Security, Breach of Web Security	CEO412.6	CO412.6	8/4/2021	24/5/2021	Semester I End Semester Exam break	AC	HOD APMC
37	What need to be Secure on Web? Can Web be secure?			12/4/2021				
38	Aspects of Web Security, Purpose of Web Security			15/4/2021				
39	A Security Equation, Defining Security Equation, Common Threats on Web,			19/4/2021	29/5/2021			
40	User level Security, Server Level Security			20/4/2021				
41	Cyber ethics, Issues in Cyber ethics			22/4/2021				

Start of Semester

Signature	Date
WJ	4/1/21
HoD	02/07

End of Semester

Signature	Date
WJ	2/7
HoD	02/07



Innovation & Leadership

Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY

Academic Year : 2020-21, Semester - II

I2IT / ACAD / CP / 04 Ver 01

PRACTICAL TEACHING RECORD

Subject Code : 414464D			Class: BE		Name of Faculty: Prof. Manjusha Amritkar			
Subject Name: Internet & Web Programming Lab					Batch: B		Teaching Scheme:	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	Monitored by	
							AC	HOD APMC
1	1.1 Using HTML5 layout tags develop informative page with sections which include various images, links to other pages for navigation, make use of all possible formatting (for example font, color etc.).	CEO412.1	CO412.1	11/1/2021, 18/1/2021	11/1/2021, 18/1/2021	Change in time table		
2	1.2 Apply CSS properties Border, margins, Padding, Navigation, dropdown list to page created in first assignment.	CEO412.1	CO412.1	25/1/2021	27/1/2021			
3	Design an online registration form for any application and validate it using JQuery	CEO412.2	CO412.2	1/2/2021, 8/2/2021	3/2/2021			
4	Design Login Application using PHP and add essence of Ajax in it	CEO412.3	CO412.3	8/3/2021	10/3/2021			
5	Create any Java Web Service and integrate it with any suitable application	CEO412.4	CO412.4	15/3/2021	17/3/2021			
6	Create JSP login page and validate it. Make use of Servlets	CEO412.3	CO412.3	1/3/2021	17/2/2021			
7	Create an application for bill payment using Angular JS	CEO412.2	CO412.2	15/2/2021, 22/2/2021	10/2/2021			

Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
8	Develop website using any CMS tool which falls into one of the categories blog, social networking, News updates, Wikipedia, E-commerce store. Website must include home page, and at least 3	CEO412.5	CO412.5	22/3/2021, 5/4/2021	7/4/2021	In semester exam break from 22nd March to 1st April			
9	Develop Mini Project using any front end tool with database connectivity	CEO412.6	CO412.6	12/4/2021, 19/4/2021	28/4/2021				

Start of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	4/1/21
HoD : <i>[Signature]</i>	04/10/21

End of Semester

Signature	Date
Course Faculty : <i>[Signature]</i>	2/7
HoD : <i>[Signature]</i>	02/10/21



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Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY

Academic Year : 2020-21, Semester - II

I2IT / ACAD / CP / 04 Ver 01

PRACTICAL TEACHING RECORD

Subject Code : 414464D			Class: BE		Name of Faculty: Prof. Manjusha Amritkar		
Subject Name: Internet & Web Programming Lab					Teaching Scheme:		
					Batch: C		
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	
1	1.1 Using HTML5 layout tags develop informative page with sections which include various images, links to other pages for navigation, make use of all possible formatting (for example font, color etc.).	CEO412.1	CO412.1	13/1/2021, 20/1/2021	13/1/2021, 20/1/2021		
2	1.2 Apply CSS properties Border, margins, Padding, Navigation, dropdown list to page created in first assignment.	CEO412.1	CO412.1	27/1/2021	1/2/2021		
3	Design an online registration form for any application and validate it using JQuery	CEO412.2	-CO412.2	3/2/2021, 10/2/2021	2/2/2021, 3/2/2021		
4	Design Login Application using PHP and add essence of Ajax in it	CEO412.3	CO412.3	3/3/2021	18/2/2021		
5	Create any Java Web Service and integrate it with any suitable application	CEO412.4	CO412.4	10/3/2021	4/3/2021		
6	Create JSP login page and validate it. Make use of Servlets	CEO412.3	CO412.3	24/2/2021	11/2/2021		
7	Create an application for bill payment using Angular JS	CEO412.2	CO412.2	17/2/2021	4/2/2021		

Change in time table

Dep
27/1

Dep
3/2

Dep
3/3

Dep
10/3

Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
8	Develop website using any CMS tool which falls into one of the categories' blog, social networking, News updates, Wikipedia, E-commerce store. Website must include home page, and at least 3	CEO412.5	CO412.5	17/3/2021, 24/3/2021	11/3/2021	In semester exam break from 22nd March to 1st April			
9	Develop Mini Project using any front end tool with database connectivity	CEO412.6	CO412.6	31/3/2021, 7/4/2021	1/4/2021, 8/4/2021				

Start of Semester

Signature	Date
Course Faculty: <i>WJ</i>	4/1/21
HoD: <i>[Signature]</i>	4/1/21

End of Semester

Signature	Date
Course Faculty: <i>WJ</i>	2/7
HoD: <i>[Signature]</i>	2/10/21



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Hope Foundation's
International Institute of Information Technology, Pune
DEPARTMENT OF INFORMATION TECHNOLOGY

Academic Year : 2020-21, Semester - II

I2IT / ACAD / CP / 04 Ver 01

PRACTICAL TEACHING RECORD

Subject Code : 414464D			Class: BE		Name of Faculty: Prof. Manjusha Amritkar					
Subject Name: Internet & Web Programming Lab					Batch: A		Teaching Scheme:		Pr : 2 Hrs / week	
Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (incase of variance)	AC	HOD	Monitored by	
1	1.1 Using HTML5 layout tags develop informative page with sections which include various images, links to other pages for navigation, make use of all possible formatting (for example font, color etc.).	CEO412.1	CO412.1	12/1/2021, 19/1/2021	12/1/2021, 19/1/2021					
2	1.2 Apply CSS properties Border, margins, Padding, Navigation, dropdown list to page created in first assignment.	CEO412.1	CO412.1	2/2/2021	1/2/2021	Lab adjustement of Prof. Sarang S				
3	Design an online registration form for any application and validate it using JQuery	CEO412.2	CO412.2	9/2/2021, 16/2/2021	2/2/2021, 9/2/2021					
4	Design Login Application using PHP and add essence of Ajax in it	CEO412.3	CO412.3	9/3/2021	2/3/2021					
5	Create any Java Web Service and integrate it with any suitable application	CEO412.4	CO412.4	16/3/2021	9/3/2021					
6	Create JSP login page and validate it. Make use of Servelets	CEO412.3	CO412.3	2/3/2021	23/2/2021					
7	Create an application for bill payment using Angular JS	CEO412.2	CO412.2	23/2/2021	16/2/2021					

Sr. No.	Experiment / Assignment	CEO	CO	Date of Plan	Date of Conduction	Remarks of Faculty (in case of variance)	Monitored by		
							AC	HOD	APMC
8	Develop website using any CMS tool which falls into one of the categories blog, social networking, News updates, Wikipedia, E-commerce store. Website must include home page, and at least 3	CEO412.5	CO412.5	23/3/2021, 30/3/2021	10/3/2021, 16/3/2021	In semester exam break from 22nd March to 1st April			
9	Develop Mini Project using any front end tool with database connectivity	CEO412.6	CO412.6	6/4/2021, 20/4/2021	6/4/2021				

Start of Semester

Signature	Date
WJ	4/1/21
HoD:	04/01

End of Semester

Signature	Date
WJ	2/7
HoD:	02/07